



**Computer Engineering Department  
Amirkabir University of Technology  
(Tehran Polytechnic)**



# **Curriculum**

## **Bachelor of Computer Engineering**

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# Bachelor of Computer Engineering



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## **Abstract:**

This curriculum is developed for a bachelor of computer engineering in the computer engineering department of Amirkabir university of technology. This curriculum is in accordance with the guidelines and regulations of the ministry of science research and technology, moreover it respects the IEEE/ACM Computer Engineering curricula and similar programs in top universities around the world. This program identifies the required capabilities of computer engineering students, and design the curriculum to meet the requirements.

## **Keywords:**

Curriculum, Bachelor, Computer Engineering



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# **Chapter 1:**

# **Introduction**



## 1-1 Objective

The purpose of the undergraduate program in computer engineering is to train students who have the ability and skills to solve computer engineering problems, including analyzing, designing, and building computer systems. This program prepares students for entrepreneurship, industry work, and graduate studies.

## 1-2 Graduates' Abilities

Students are expected to have the following abilities and capabilities after successful completion of this curriculum:

- 1) Analysis, design, and implementation of computer systems
- 2) Understand, formulate, and solve problems
- 3) Applying math, science, and engineering knowledge to solve real-world problems
- 4) Applying techniques, skills and world-class tools to solve engineering problems
- 5) Ability to learn and apply new knowledge after graduation
- 6) Effective and responsible performance as a member of a team
- 7) Communicate effectively spoken and written
- 8) Adherence to the principles of professional ethics and social responsibility

## 1-3 Program Structure

The undergraduate program in Computer Engineering consists of 140 practical and theoretical units in five categories: General (20 units), Basic (20 units), Main (55 units), Specialized (30 units), and Optional (15 units). The overall structure of the program is depicted in Figure 1.

Passing all the general, basic, and main courses are required for all computer engineering students. The specialized courses are offered as a set of packages. Each package consists of eight three-unit lessons (24 units in total). Each student selects two packages of the packages and receives 18 units of one of the packages and 12 units of the other one. Students must obtain 15 optional units from the list of courses specified in the optional lesson table.

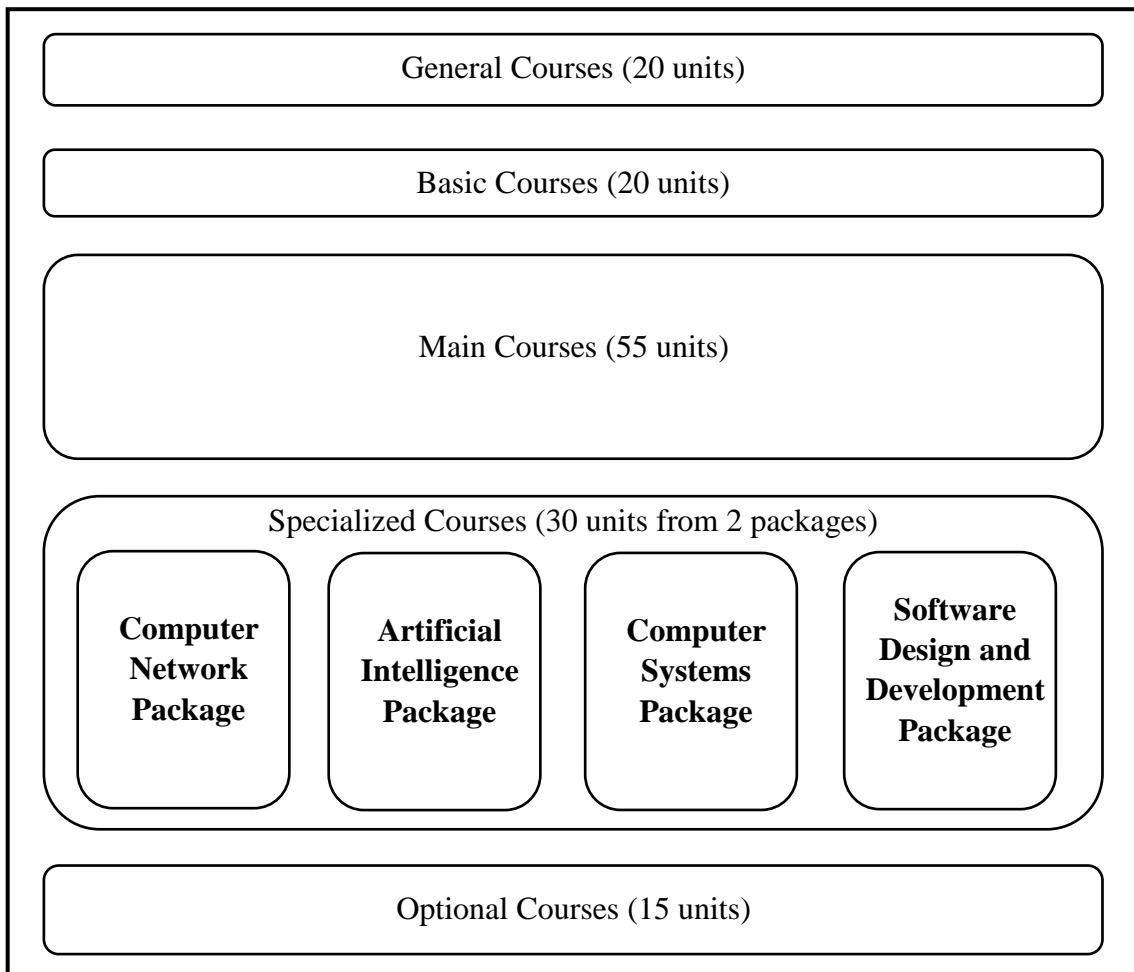


Figure 1 - Overall composition of the 140 units of computer engineering undergraduate courses

These packages are briefly described in the following.

**Software Design and Development Package:** The purpose of this package is to familiarize students with all the steps needed to design and develop a high-quality software system. Students in this pack will gain all the necessary knowledge about algorithm design techniques, database design principles, software project management, and software testing and design techniques. Understanding a few programming languages and compiler design principles are the other goals of this specialized package. Students who successfully complete courses in this specialized package can enroll in software development firms to pursue a job market or pursue a master's degree in related fields.

**Computer Systems Package:** Computer Systems package is related to the subjects of computer engineering that include design, manufacturing, implementation, and maintenance of software and hardware components of modern computing systems as well as computer-controlled devices. Students who take courses from this specialized package





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can be involved in the computer systems industry and research groups from design to implementation of the systems. Examples of computer systems can be found in components of digital products such as digital cameras, tablets, automotive electronic control systems, digital medical devices, communication devices, and smart home appliances. In some of the courses of this package, the student will find the skills needed to design and build a complete digital system consisting of hardware and software. In addition, in some courses, students become familiar with the concepts of fast data processing in data centers, such as cloud computing.

**Artificial Intelligence Package:** The purpose of this package is to get students familiar with the concepts and basics of artificial intelligence, designing intelligent algorithms and intelligent data processing, moreover, it aims to teach problem-solving tools and methods such as logic, evolutionary algorithms, neural networks, and fuzzy systems. Students also become familiar with the applications of artificial intelligence in robotics, data mining, data recovery, and bioinformatics. The emphasis of the courses is based on the practical applications of artificial intelligence techniques. Interested students can choose some of their optional courses from the master of artificial intelligence courses, depending on their interests and abilities.

**Computer Network Package:** The purpose of this pack is to familiarize students with the concepts and fundamentals of computer networks and distribution systems. The courses of this package emphasize the theoretical and practical capabilities needed to prepare students for entrance into the labor market and entrepreneurship related to computer networks and network-based systems. Areas of interest in this package include the principles of wireless network design, web application development, applications for smart devices such as smartphones, cloud computing services, and computer network security. Students will be prepared to pursue higher education in the related fields and with choosing from master courses in computer networks and cloud computing rather than the package courses, as optional courses, they can deepen their knowledge and skills.



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# **Chapter 2:**

# **Courses List**



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## 2-1 General Courses

These courses are specified by the education committee of the university rather than this curriculum.

Each student should take 20 units of the courses in this category.



## 2-2 Basic Courses

Basic Courses				
No.	Title	Units	Type	Prerequisite
1	Mathematics I	3	Theory	---
2	Mathematics II	3	Theory	Mathematics I
3	Physics I	3	Theory	---
4	Physics II	3	Theory	Mathematics I
5	Differential Equations	3	Theory	Mathematics I
6	Engineering Probability and Statistics	3	Theory	Mathematics II
7	Physics Laboratory II	1	Practical	Physics II
8	General/Electrical Workshop	1	Practical	---
<b>Total Units</b>		<b>20</b>		



## 2-3 Main Courses

Main Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE101	Fundamentals of Computer and Programming	3	Theory	---	Fundamentals of Computer and Programming Workshop
2	CE102	Fundamentals of Computer and Programming Workshop	1	Practical	---	Fundamentals of Computer and Programming
3	CE103	Discrete Mathematics	3	Theory	Mathematics I, Fundamentals of Computer and Programming	---
4	CE104	Advanced Programming	3	Theory	Fundamentals of Computer and Programming	Advanced Programming Workshop
5	CE105	Advanced Programming Workshop	1	Practical	---	Advanced Programming
6	CE201	Logic Circuits	3	Theory	---	Discrete Mathematics, Logic Circuits Laboratory
7	CE202	Logic Circuits Laboratory	1	Practical	---	Logic Circuits
8	CE203	Data Structures and Algorithms	3	Theory	Discrete Mathematics, Advanced Programming	---
9	CE204	Electrical and Electronic Circuits	3	Theory	Physics II	Differential Equations, Electrical and Electronic Circuits Laboratory
10	CE205	Electrical and Electronic Circuits Laboratory	1	Practical	---	Electrical and Electronic Circuits



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## Main Courses

No.	Code	Title	Units	Type	Prerequisite	Corequisite
11	CE206	Technical English	2	Theory	English Language II	---
12	CE207	Computer Architecture	3	Theory	Logic Circuits	Computer Architecture Laboratory
13	CE208	Computer Architecture Laboratory	1	Practical	---	Computer Architecture
14	CE209	Theory of Languages and Machines	3	Theory	Data Structures and Algorithms	---
15	CE210	Applied Linear Algebra	3	Theory	Mathematics II	---
16	CE301	Microprocessor and Assembly Language	3	Theory	Computer Architecture	Microprocessor and Assembly Language Laboratory
17	CE302	Microprocessor and Assembly Language Laboratory	1	Practical	---	Microprocessor and Assembly Language
18	CE303	Operating Systems	3	Theory	Computer Architecture	Operating Systems Laboratory
19	CE304	Operating Systems Laboratory	1	Practical	---	Operating Systems
20	CE305	Computer Networks	3	Theory	Engineering Probability and Statistics, Computer Architecture	Operating Systems, Computer Networks Laboratory
21	CE306	Computer Networks Laboratory	1	Practical	---	Computer Networks
22	CE307	Software Engineering I	3	Theory	Advanced Programming	---
23	CE308	Research and Presentation Methods	2	Theory	Technical English	---
24	CE309	Internship	1	Practical	Research and Presentation Methods	---



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Main Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
25	CE401	Final Project	3	Practical	Research and Presentation Methods	---
<b>Total Units</b>			<b>55</b>			



## 2-4 Packages Courses

### 2-4-1 Software Design and Development Package

Software Design and Development Package Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE221	Algorithm Design	3	Theory	Data Structures and Algorithms	---
2	CE231	Principles of Database Design	3	Theory	Data Structures and Algorithms	---
3	CE232	Compiler Design	3	Theory	Data Structures and Algorithms	---
4	CE233	Programming Languages	3	Theory	Theory of Languages and Machines	---
5	CE331	Software Engineering II	3	Theory	Software Engineering I	---
6	CE431	Software Testing	3	Theory	Software Engineering II	---
7	CE432	User Interface Design	3	Theory	Software Engineering I	---
8	CE421	Information Retrieval	3	Theory	Data Structures and Algorithms, Engineering Probability and Statistics	---
<b>Total Units</b>			<b>24</b>			





## 2-4-2 Computer Systems Package

Computer Systems Package Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE222	Signal and Systems	3	Theory	Differential Equations	---
2	CE241	Interface Circuits Design	3	Theory	Microprocessor and Assembly Language	---
3	CE242	Programmable Digital Systems Design	3	Theory	Computer Architecture	---
4	CE243	Digital Electronics	3	Theory	Electrical and Electronic Circuits	---
5	CE341	Embedded and Real-Time Systems	3	Theory	Operating Systems, Microprocessor and Assembly Language	---
6	CE342	Multi-Processor Programming	3	Theory	Operating Systems	---
7	CE422	Fundamentals of Cloud Computing	3	Theory	Computer Networks, Operating Systems	---
8	CE441	Hardware-Software Codesign	3	Theory	Computer Architecture	---
<b>Total Units</b>			<b>24</b>			



## 2-4-3 Artificial Intelligence Package

Artificial Intelligence Package Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE221	Algorithm Design	3	Theory	Data Structures and Algorithms, Discrete Mathematics	---
2	CE222	Signal and Systems	3	Theory	Differential Equations	---
3	CE251	Principles and Applications of Artificial Intelligence	3	Theory	Data Structures and Algorithms	Applied Linear Algebra
4	CE351	Foundations of Computational Intelligence	3	Theory	Algorithm Design	---
5	CE352	Introduction to Robotics	3	Theory	Signal and Systems	---
6	CE451	Introduction to Bioinformatics	3	Theory	Foundations of Computational Intelligence	---
7	CE452	Data Mining	3	Theory	Foundations of Computational Intelligence	---
8	CE421	Information Retrieval	3	Theory	Data Structures and Algorithms, Engineering Probability and Statistics	---
<b>Total Units</b>			<b>24</b>			



## 2-4-4 Computer Network Package

Computer Network Package Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE222	Signal and Systems	3	Theory	Differential Equations	---
2	CE261	Web Programming	3	Theory	Computer Networks	---
3	CE361	Data Communication	3	Theory	Computer Networks, Signal and Systems	---
4	CE362	Information and Communications Security	3	Theory	Computer Networks	---
5	CE363	Multimedia Systems	3	Theory	Engineering Probability and Statistics, Signal and Systems	---
6	CE364	Mobile Device Programming	3	Theory	Advanced Programming	---
7	CE422	Fundamentals of Cloud Computing	3	Theory	Computer Networks, Operating Systems	---
8	CE461	Internet of Things Fundamentals	3	Theory	Microprocessor and Assembly Language, Computer Networks	---
<b>Total Units</b>			<b>24</b>			



## 2-4 Optional Courses

Optional Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
1	CE371	Computer Graphics	3	Theory	Advanced Programming	---
2	CE372	Human and Computer Interaction	3	Theory	Software Engineering I	---
3	CE373	Robot Building Workshop	3	Theory	Introduction to Robotics	---
4	CE374	Computer Games Design	3	Theory	Advanced Programming	---
5	CE375	Theory of Computation	3	Theory	Theory of Languages and Machines	---
6	CE376	Computer Simulation	3	Theory	Operating Systems, Engineering Probability and Statistics	---
7	CE377	Foundations of Computer Animation	3	Theory	Computer Graphics	---
8	CE378	Information Technology Project Management	3	Theory	---	---
9	CE379	E-Commerce	3	Theory	Engineering Economics, Computer Networks	---
10	CE380	Startup Business Development	3	Theory	Software Engineering I	---
11	CE381	Industrial Automation	3	Theory	Microprocessor and Assembly Language	---
12	CE382	Special Topics 1	3	Theory	---	---
13	CE383	Special Topics 2	3	Theory	---	---
14	---	Linear Control (Electrical Engineering)	3	Theory	---	---
15	---	Engineering Economics (Industrial Engineering)	3	Theory	---	---



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Optional Courses						
No.	Code	Title	Units	Type	Prerequisite	Corequisite
16	---	Project Control (Industrial Engineering)	3	Theory	---	---
17	---	Graph Theory (Computer Science)	3	Theory	---	---
18	---	Numerical Computation	3	Theory	---	---
19	---	Up to 5 courses from the non-selected Packages	3	Theory	---	---
20	---	Up to 2 courses from other departments (need department approval)	3	Theory	---	---
21	---	Up to 2 master courses (need department approval)	---	---	---	---



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# **Chapter 3:**

# **Courses Syllabus**



## 3-1 Main Courses

### 3-1-1 Fundamentals of Computer and Programming (CE101)

Fundamentals of Computer and Programming (CE101)		
Units	Prerequisites	Corequisites
3	---	Fundamentals of Computer and Programming Workshop
<b>Objectives:</b> The main objective of this course is to learn how to solve problems with computers which is achieved by understanding how computer works as a computational machine, learning algorithmic thinking to solve problems, and learning how to describe an algorithm using a programming language (e.g., C)		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to computer<ul style="list-style-type: none"><li>○ History</li><li>○ Hardware components</li><li>○ Software components</li></ul></li><li>• Problem solving by computer<ul style="list-style-type: none"><li>○ Problem solving steps</li><li>○ Algorithm design</li><li>○ Algorithm description</li></ul></li><li>• Programming basic steps<ul style="list-style-type: none"><li>○ Programming languages types</li><li>○ Programming procedure</li></ul></li><li>• Basic steps of the programming language<ul style="list-style-type: none"><li>○ The main function</li><li>○ Variables</li><li>○ Types</li><li>○ Values</li></ul></li><li>• Developing simple programs<ul style="list-style-type: none"><li>○ Basic input/output</li><li>○ Mathematical calculations</li></ul></li><li>• Loop and Condition statements</li><li>• Procedural programming and function</li><li>• Recursive functions</li></ul>		



- Debugging
- Arrays
- Pointers
- Characters and Strings
- Complex data structures
- File
- Direct access to hardware (optional)

**References:**

- [1] Paul Deitel, Harvey Deitel, *C How to Program*, 8<sup>th</sup> Edition, Pearson Education, 2015
- [2] David Griffiths, Dawn Griffiths, *Head First C*, O'Reilly, 2012





## 3-1-2 Fundamentals of Computer and Programming Workshop (CE102)

Fundamentals of Computer and Programming (CE102)		
Units	Prerequisites	Corequisites
1	---	Fundamentals of Computer and Programming
<b>Objectives:</b> This lab is corequisites of the “Fundamentals of Computer and Programming” course. Its objective to improve the programming skill of students. In this course, students develop programs based on concepts they are learning CE101.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to computer hardware components (I/O, CPU, Mainboard, Cards, ...)</li><li>• Familiarize with different OSs (Linux, Windows, MacOS)</li><li>• Familiarize with applications (Browsers, Office, IDE, ...)</li><li>• Developing basic programs in IDE</li><li>• Developing programs with mathematical calculations</li><li>• Developing programs to interact with user via formatted console I/O</li><li>• Developing programs need making decisions</li><li>• Developing programs need iterations</li><li>• Developing complex programs using functions</li><li>• Developing programs need arrays</li><li>• Developing programs need multiple functions and arrays</li><li>• Developing programs need dynamic memory (heap) using pointers</li><li>• Developing programs to work with struct</li><li>• Developing programs to work with files</li></ul>		
<b>References:</b> [1] Paul Deitel, Harvey Deitel, <i>C How to Program</i> , 8 <sup>th</sup> Edition, Pearson Education, 2015 [2] David Griffiths, Dawn Griffiths, <i>Head First C</i> , O'Reilly, 2012		



## 3-1-3 Discrete Mathematics (CE103)

Discrete Mathematics (CE103)		
Units	Prerequisites	Corequisites
3	Calculus I, Fundamentals of Computer and Programming	---
<b>Objectives:</b> The course consists of the topics of discrete mathematics that are extensively used in other courses of bachelor of computer science and engineering. This course is meant to acquaint students with the logical foundations of reasoning and to make more effective the students' problem solving abilities. The course contains topics of set theory, logic, mathematical induction, counting, relations and functions, recurrence relations, number theory, and graph theory.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Counting<ul style="list-style-type: none"><li>○ Principles of Counting</li><li>○ Inclusion and Exclusion</li><li>○ Generating Functions</li></ul></li><li>• Logic<ul style="list-style-type: none"><li>○ Propositional Logic</li><li>○ First-Order Logic</li><li>○ Boolean Algebra</li></ul></li><li>• Induction<ul style="list-style-type: none"><li>○ Mathematical Induction</li><li>○ Inductive Definitions</li><li>○ Structural Induction</li></ul></li><li>• Integers<ul style="list-style-type: none"><li>○ Divisibility</li><li>○ Diophantine Equations</li><li>○ Modular Arithmetic</li></ul></li><li>• Relations and Functions<ul style="list-style-type: none"><li>○ Partial Orders and Equivalence Relations</li><li>○ Countable and Infinite Sets</li><li>○ Time Complexity</li></ul></li><li>• Recurrence Relations<ul style="list-style-type: none"><li>○ Recursive Formulation of Problems</li><li>○ Linear Homogeneous and Nonhomogeneous Recurrence Relations</li></ul></li></ul>		



- Graph Theory
  - Euler Circuits
  - Hamilton Cycles
  - Graph Coloring
  - Tree

**References:**

- [1] R. P. Grimaldi, *Discrete and Combinatorial Mathematics: An Applied Introduction*, 5<sup>th</sup> Edition, Pearson, 2017.
- [2] K. H. Rosen, *Discrete Mathematics and Its Applications*, 7<sup>th</sup> Edition, McGraw-Hill, 2011.
- [3] S. S. Epp, *Discrete Mathematics with Applications*, 4<sup>th</sup> Edition, Brooks Cole, 2010



## 3-1-4 Advanced Programming (CE104)

Advanced Programming (CE104)		
Units	Prerequisites	Corequisites
3	Fundamentals of Computer and Programming	Advanced Programming Workshop
<b>Objectives:</b> The purpose of the course is to study the fundamental concepts and techniques necessary to write high-quality programs. In order to deal with the complexities of medium to large scale programs, topics such as top-down design and basic concepts of object-oriented programming are covered. Additionally, the course focuses on program verification, testing, and debugging. An object-oriented programming language (e.g. Java) is utilized to convey the concepts and techniques of OOP stepwise design to students. Other advanced topics such as multi-threading, GUI libraries, and network programming are also covered.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to large scale programming and high quality program attributes</li><li>• Top-down design, modeling the real-world using object-oriented approach</li><li>• UML Modeling</li><li>• Object-Oriented Programming<ul style="list-style-type: none"><li>○ Classes and Objects</li><li>○ Inheritance</li><li>○ Polymorphism</li><li>○ I/O Library Classes and other Library Classes</li></ul></li><li>• Testing and debugging<ul style="list-style-type: none"><li>○ Testing and Fault detection</li><li>○ Exception Handling</li></ul></li><li>• Standard Data Structures<ul style="list-style-type: none"><li>○ Collections</li><li>○ Queue and Stack</li><li>○ Text processing and Strings</li></ul></li><li>• Advanced Concepts and Techniques<ul style="list-style-type: none"><li>○ GUI Programming</li><li>○ Graphics Programming</li><li>○ Multi-Threaded Programming</li><li>○ Network Programming</li><li>○ Generic Programming</li><li>○ Reflection and its Uses</li><li>○ Connecting to Standard Databases</li></ul></li></ul>		



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**References:**

- [1] D. Barnes, M. Kolling, *Object First with Java*, 5<sup>th</sup> Edition, Pearson, 2012.
- [2] H. Deitel, P. Deitel, *Java How to Program*, 9<sup>th</sup> Edition, Prentice-Hall, 2012.



## 3-1-5 Advanced Programming Workshop (CE105)

Advanced Programming Workshop (CE105)		
Units	Prerequisites	Corequisites
1	---	Advanced Programming
<b>Objectives:</b> Advanced Programming Lab is offered along with the Advanced Programming Course. It aims to provide the opportunity to implement concepts learned in the advanced programming course. In this lab, in addition to conducting small weekly projects, students carry out a major programming project throughout the semester		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Understanding UML and building models using UML</li><li>• Creating and working with classes and objects</li><li>• Implementation of inheritance and polymorphic behavior among classes</li><li>• Implementation of common text processing concepts</li><li>• Creation of GUI with various components</li><li>• Implementation of a multi-threaded program</li><li>• Implementation of a program distributed over a network</li><li>• Implementing Generic classes and methods</li><li>• Implementation and application of reflection</li><li>• Connecting to a database</li><li>• A large-scale programming project</li></ul>		
<b>References:</b> [1] D. Barnes, M. Kolling, <i>Object First with Java</i> , 5 <sup>th</sup> Edition, Pearson, 2012. [2] H. Deitel, P. Deitel, <i>Java How to Program</i> , 9 <sup>th</sup> Edition, Prentice-Hall, 2012.		



## 3-1-6 Logic Circuits (CE201)

Logic Circuits (CE201)		
Units	Prerequisites	Corequisites
3	---	Discrete Mathematics, Logic Circuits Laboratory
<b>Objectives:</b> This course has two main objectives: <ul style="list-style-type: none"><li>• Introduction to basic concepts and components of logic circuits and their operation.</li><li>• To develop skills in designing digital circuits and systems and analyzing them in terms of their functionalities and speed</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to logic circuits and basic concepts</li><li>• Numerical systems</li><li>• Boolean algebra</li><li>• Logic gates</li><li>• Analysis and design of combinational circuits</li><li>• Simplification techniques for combinational circuits<ul style="list-style-type: none"><li>○ Boolean algebra</li><li>○ Karnaugh map</li></ul></li><li>• Basic block circuits and their applications<ul style="list-style-type: none"><li>○ Multiplexer</li><li>○ Demultiplexer</li><li>○ Encoder</li><li>○ Decoder</li><li>○ 7-segment</li></ul></li><li>• Arithmetic circuits<ul style="list-style-type: none"><li>○ Adder</li><li>○ Subtractor</li><li>○ Comparator</li><li>○ Multiplier</li></ul></li><li>• Electronic circuits of logic gates</li><li>• Basic electronics concepts of logic circuits<ul style="list-style-type: none"><li>○ High-impedance</li><li>○ Pull-up/pull-down resistors</li><li>○ Wired logic</li><li>○ Delay</li></ul></li></ul>		



- Basic elements of sequential circuits
  - Latch
  - Flip-Flop
- Analysis and design of sequential circuits
- Sequential circuits optimization techniques

**References:**

- [1] C. Roth, L. Kinney, *Fundamentals of Logic Design*, 7<sup>th</sup> Edition, Cengage Learning, 2014.
- [2] F. Vahid, *Digital Design with RTL Design, VHDL, and Verilog*, 2<sup>nd</sup> Edition, Wiley, 2011.
- [3] M. Mano, *Digital Design: With an Introduction to the Verilog HDL, VHDL, and SystemVerilog*, 6<sup>th</sup> Edition, 2017.
- [4] R. H. Katz, G. Borriello, *Contemporary Logic Design*, 2<sup>nd</sup> Edition, Benjamin Cummings /Addison Wesley, 2004.





## 3-1-7 Logic Circuits Laboratory (CE202)

Logic Circuits lab rotary (CE202)		
Units	Prerequisites	Corequisites
1	---	Logic Circuits
<b>Objectives:</b> This laboratory has two main objectives: <ul style="list-style-type: none"><li>• To gain knowledge about commercial logic circuits components and their operations</li><li>• To develop skills in designing digital circuits and systems</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to laboratory equipment</li><li>• Basic logic gates</li><li>• Implementation of logic functions by Karnaugh map</li><li>• Hardware description language</li><li>• Multiplexers, de-multiplexers and decoders</li><li>• Implementation of an adder/subtractor</li><li>• Code converter</li><li>• Arithmetic/logic unit</li><li>• Latches and flip-flops</li><li>• Timers</li><li>• A project on sequential circuit design</li></ul>		
<b>References:</b> [1] C. Roth, L. Kinney, <i>Fundamentals of Logic Design</i> , 7 <sup>th</sup> Edition, Cengage Learning, 2014. [2] F. Vahid, <i>Digital Design with RTL Design, VHDL, and Verilog</i> , 2 <sup>nd</sup> Edition, Wiley, 2011. [3] M. Mano, <i>Digital Design: With an Introduction to the Verilog HDL, VHDL, and SystemVerilog</i> , 6 <sup>th</sup> Edition, 2017. [4] R. H. Katz, G. Borriello, <i>Contemporary Logic Design</i> , 2 <sup>nd</sup> Edition, Benjamin Cummings /Addison Wesley, 2004.		



## 3-1-8 Data Structures and Algorithms (CE203)

Data Structures and Algorithms (CE203)		
Units	Prerequisites	Corequisites
3	Discrete Mathematics, Advanced Programming	---
<b>Objectives:</b> The goal of this course is introduction to various types of data structures for management of data in main memory and secondary storage of computers, and also introduction to methods of designing of algorithms.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Analysis of Algorithms<ul style="list-style-type: none"><li>○ Growth of functions</li><li>○ Recursive relations and methods of solving them (guess and induction, substitution, master theorem)</li><li>○ Amortized analysis</li></ul></li><li>• Various Types of Lists<ul style="list-style-type: none"><li>○ One-way Lists</li><li>○ Two-ways Lists</li><li>○ Queues, Stacks</li><li>○ Lists Operations</li><li>○ Applications of Lists (mathematical expressions, garbage collection, merge sort)</li></ul></li><li>• Trees<ul style="list-style-type: none"><li>○ Initial definitions</li><li>○ Expression tree</li><li>○ Various tree representations</li><li>○ Induction on trees</li><li>○ Exploration of trees</li><li>○ Structural induction</li><li>○ Binary trees</li><li>○ Operations on expression trees</li><li>○ Converting various types of expressions</li><li>○ Tries</li><li>○ Binary search trees</li></ul></li><li>• Hashing<ul style="list-style-type: none"><li>○ Cascading</li><li>○ Global hashing</li></ul></li></ul>		



- Open hashing
- Sorting and Order Statistics
  - Lower bound
  - Decision tree
  - Linear-time sorting (counting, radix, and bucket sorts)
  - Quick sort
  - Heap sort
  - Statistical sort
  - External sort
- Main methods of Algorithm Design
  - Divide and conquer
  - Dynamic programming
  - Greedy approach
  - Backtracking method
  - Branch and Bound

**References:**

[1] T. Cormen, C. Leiserson, R. Rivest, *Introduction to Algorithm*, McGraw-Hill, 2001



## 3-1-9 Electrical and Electronic Circuits (CE204)

Electrical and Electronic Circuits (CE204)		
Units	Prerequisites	Corequisites
3	Physics II	Differential Equations, Electrical and Electronic Circuits Laboratory
<b>Objectives:</b> The goal of this course is to introduce students to basic concepts and principles of DC and AC circuits theory and analysis. It provides skills to develop and solve mathematical representations of circuits, as well as to analyze the dynamic behavior of electrical and electronic circuits.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Basic concepts<ul style="list-style-type: none"><li>○ Lumped and distributed circuits</li><li>○ Kirchhoff's voltage and current laws</li></ul></li><li>• One-port circuit elements<ul style="list-style-type: none"><li>○ Resistor</li><li>○ Capacitor</li><li>○ Inductor</li><li>○ Diode</li><li>○ Independent voltage and current source</li></ul></li><li>• Two-port elements<ul style="list-style-type: none"><li>○ Dependent source and its application in modeling transistors and operational amplifiers</li></ul></li><li>• Energy and power<ul style="list-style-type: none"><li>○ Active elements</li><li>○ Passive elements</li></ul></li><li>• Analysis of simple resistive circuits<ul style="list-style-type: none"><li>○ Series and parallel connections</li><li>○ Voltage and current division</li><li>○ Source transformation</li><li>○ Basic nodal and mesh analysis</li><li>○ Thevenin and Norton equivalent circuits</li><li>○ Superposition principle</li><li>○ Maximum power transfer</li></ul></li><li>• 1st order circuits<ul style="list-style-type: none"><li>○ linear and time-independent elements</li></ul></li></ul>		



- Source-free and complete response
- Natural and forced response
- Step response and impulse response and their relation
- 2nd order circuits
  - Source-free and complete response
  - Natural and forced response
  - Step and impulse response
  - Dual circuits
  - Use of convolution to determine circuit response
- Sinusoidal steady-state analysis
  - Impedance and admittance
  - Phasor
  - Frequency response
- Non-linear resistive circuits
  - Introduction of diode
  - Mathematical and graphical analysis of resistive-diode circuits
  - Rectifier circuits
- Amplifiers
  - Introduction of BJT and MOSFET as amplifiers
  - Circuit modeling using dependent sources
  - Input/output impedance
  - Thevenin and Norton equivalent circuits
  - DC analysis and operating point
- Operational amplifiers
  - Circuit modeling using dependent sources
  - Analysis of simple OPAMP circuits
  - Practical examples (amplifier, current source, filter, integrator and differentiator, voltage follower, etc.)

## References:

- [1] W. H. Hayt, J. E. Kemmerly, and S. M. Durbin, *Engineering Circuit Analysis*, 8<sup>th</sup> Edition, McGraw Hill, 2011.
- [2] C. K. Alexander, M. N. O. Sadiku, *Fundamentals of Electrical Circuits*, 5<sup>th</sup> Edition, McGraw Hill, 2012.
- [3] A. Agarwal, J. H. Lang, *Foundations of Analog and Digital Electronic Circuits*, Morgan Kaufmann Publishers, 2005.



## 3-1-10 Electrical and Electronic Circuits Laboratory (CE205)

Electrical and Electronic Circuits Laboratory (CE205)		
Units	Prerequisites	Corequisites
1	---	Electrical and Electronic Circuits
<b>Objectives:</b> The goal of this lab is to let students get familiar with electrical circuit elements and lab equipment (e.g. oscilloscope, multimeter, power supply, signal generator, ...) and learn how to work with them. They will also learn how to simulate, implement, test and debug practical electrical and electronic circuits.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Getting familiar with circuit elements and lab equipment</li><li>• Examining the Ohm's law and Kirchhoff's current and voltage laws</li><li>• Examining the Norton and Thevenin equivalent circuits</li><li>• Getting familiar with Orcad Capture simulation tool<ul style="list-style-type: none"><li>○ Running DC Bias analysis</li><li>○ DC Sweep analysis</li><li>○ AC analysis</li><li>○ Time Domain analysis</li></ul></li><li>• RL and RC transient response analysis<ul style="list-style-type: none"><li>○ Implementing RL and RC low-pass</li><li>○ Band-pass and high-pass filters</li><li>○ Examining unit-step response</li></ul></li><li>• RLC transient response analysis<ul style="list-style-type: none"><li>○ Implementing RLC circuits</li><li>○ Examining unit-step response</li></ul></li><li>• RC frequency response analysis<ul style="list-style-type: none"><li>○ Implementing and analyzing RC low-pass and band-pass filters and RC integrator circuit</li></ul></li><li>• Getting familiar with diodes and its applications (rectifier, clamper, clipper)</li><li>• Getting familiar with MOSFET transistor as an amplifier and as a switch, learn how to bias a transistor</li><li>• Getting familiar with Opamps, its application as an inverting and non-inverting amplifiers, active filters and integrator</li><li>• Final project</li></ul>		



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**References:**

- [1] W. H. Hayt, J. E. Kemmerly, and S. M. Durbin, *Engineering Circuit Analysis*, 8<sup>th</sup> Edition, McGraw Hill, 2011.
- [2] C. K. Alexander, M. N. O. Sadiku, *Fundamentals of Electrical Circuits*, 5<sup>th</sup> Edition, McGraw Hill, 2012.
- [3] A. Agarwal, J. H. Lang, *Foundations of Analog and Digital Electronic Circuits*, Morgan Kaufmann Publishers, 2005.



## 3-1-11 Technical English (CE206)

Technical English (CE206)		
Units	Prerequisites	Corequisites
2	English Language II	---
<b>Objectives:</b> The purpose of this course is to improve student's abilities and skills in understanding technical articles and lectures in Computers Engineering. Variety of technical subjects are presented using motivating texts and talks to improve the students' English skills. Topics reflect the latest developments in technology in Computer and IT domain.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Operating Systems (Introduction to Unix)</li><li>• People in Computing (Familiarity with different fields of CE and IT, familiarity with writing CV)</li><li>• Software Engineering and Object Oriented Programming</li><li>• Recent Developments in CE/IT/AI</li><li>• The Future of CE/IT/AI</li><li>• Computing Support</li><li>• Data Security</li><li>• Multimedia</li><li>• Graphical User Interface</li><li>• Website Development</li><li>• Computer Architecture</li><li>• The Internet</li><li>• Applications Programs</li><li>• Web Design and Development</li><li>• Database Systems</li><li>• Cloud Computing</li><li>• Search Engine and Search Engine Optimization</li><li>• Social Networks</li><li>• Mobile Applications</li><li>• Computer Security</li><li>• Virtual Reality</li><li>• Computer Networks</li><li>• Artificial Intelligent</li></ul>		





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**References:**

- [1] E. H. Glendinning , J. McEwan, *Oxford English for Information Technology*, Oxford University Press, 2006.



## 3-1-12 Computer Architecture (CE207)

Computer Architecture (CE207)		
Units	Prerequisites	Corequisites
3	Logic Circuits	Computer Architecture Laboratory
<p><b>Objectives:</b></p> <p>Computer Architecture courses introduces the structure and organization of a basic computer, memory units, central processing unit, and input/output devices. The detailed architecture of a processor, Register-Transfer Language (RTL), Arithmetic and Logic Unit (ALU), various arithmetic components (adders, subtractors, multipliers, and dividers), Von-Neumann algorithm, data-path and control unit design, memory hierarchy, and performance evaluation are discussed in this course. One of the popular Hardware Description Languages (HDL) is used for design and simulation of the components.</p>		
<p><b>Topics:</b></p> <ul style="list-style-type: none"><li>• Introduction and basic concepts<ul style="list-style-type: none"><li>○ Definitions and history</li><li>○ Performance</li><li>○ Amdahl's Law</li></ul></li><li>• Instructions and computer language<ul style="list-style-type: none"><li>○ Number and instruction presentations</li><li>○ ISA</li><li>○ Addressing mode</li><li>○ RISC and CISC concepts</li></ul></li><li>• Computer arithmetic<ul style="list-style-type: none"><li>○ ALU</li><li>○ Adders/Subtractors</li><li>○ Multipliers</li><li>○ Dividers</li><li>○ Floating-point unit</li></ul></li><li>• Processing unit<ul style="list-style-type: none"><li>○ RTL</li><li>○ Von-Neumann model</li><li>○ Data-path</li><li>○ Control unit</li><li>○ Micro-operation</li><li>○ Pipeline</li></ul></li></ul>		



- I/O
- DMA
- Memory hierarchy
  - Memory wall
  - Static and dynamic memory
  - Cache memory
  - Cache placement and replacement algorithms
- Parallel processing
  - Technology trend
  - Flynn taxonomy
  - GPUs
  - Clusters
  - Multicore

**References:**

- [1] D. Patterson, J. Hennessy, *Computer Organization and Design: The Hardware/Software Interface*, Morgan Kaufmann, 5<sup>th</sup> Edition, 2013.
- [2] M. Mano, *Computer System Architecture*, Prentice Hall, 3<sup>rd</sup> Edition, 1992.
- [3] M. Mano, C.R. Kimi, *Logic and Computer Design Fundamentals*, 3<sup>rd</sup> Edition, Prentice-Hall, 2004.
- [4] W. S. Stalling, *Computer Organization and Architecture*, 6<sup>th</sup> Edition, Prentice-Hall, 2003.
- [5] V. C. Hamacher, Z. G. Vrasenic, and S. G. Zaky, *Computer Organization*, McGraw-Hill, 3<sup>rd</sup> Edition, 1990.



## 3-1-13 Computer Architecture Laboratory (CE208)

Computer Architecture Laboratory (CE208)		
Units	Prerequisites	Corequisites
1	---	Computer Architecture
<b>Objectives:</b> The aim of this laboratory is to acquaint students with sequential circuit design, analysis and debugging, ALU design and implementation, control unit design, basic computer design and implementation, bus issues and IO systems. Moreover, students will learn to describe, synthesis and practical implementation on FPGA chips.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• VHDL basics<ul style="list-style-type: none"><li>○ Basic circuit description</li><li>○ Simulation</li></ul></li><li>• Basic circuits<ul style="list-style-type: none"><li>○ Examples: decoder, encoder, multiplexer and 4-bit comparator</li></ul></li><li>• Sequential circuits<ul style="list-style-type: none"><li>○ Process</li><li>○ Counter implementation</li></ul></li><li>• Adders<ul style="list-style-type: none"><li>○ Implementation</li><li>○ Delay-area analysis</li></ul></li><li>• Multipliers</li><li>• Delay in digital circuits</li><li>• Memories<ul style="list-style-type: none"><li>○ Memory design,</li><li>○ ROM, RAM and CAM</li></ul></li><li>• Dividers</li><li>• Basic computer design and implementation (model I)</li><li>• Basic computer design and implementation (model II)</li><li>• Control unit design</li><li>• Pipeline<ul style="list-style-type: none"><li>○ Implementation</li><li>○ Speedup analysis</li></ul></li><li>• Syn./Async. uni-/bi- direction bus implementation</li><li>• Bus arbitration implementation</li></ul>		



- Floating-point adders/subtractors
- Performance evaluation
  - How to find IPC, CPI, MIPS and DMIPS of a typical processor
- Memory hierarchy

**References:**

- [1] S. Brown, Z. Vranesic, *Fundamentals of Digital Logic with Verilog Design*, McGraw-Hill, 2003.
- [2] B. Parhami, *Computer Arithmetic – Algorithms and Hardware Designs*, Oxford University Press, 2000.
- [3] D.A. Patterson, J.L. Hennessey, *Computer Organization and Design: The Hardware, Software Interface*, Morgan Kaufmann, 5<sup>th</sup> Edition, 2013.
- [4] D.M Harris, *Digital Design and Computer Architecture*, 2<sup>nd</sup> Edition, Morgan Kaufman, 2012.



## 3-1-14 Theory of Languages and Machines (CE209)

Theory of Languages and Machines (CE209)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms	---
<b>Objectives:</b> This course familiarizes students with the foundations and principles of computer science and discusses material that is useful in subsequent courses. This course also strengthens students' ability to carry out formal and rigorous mathematical arguments.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Fundamental concepts in Formal Languages and Automata Theory</li><li>• Theory of Regular Languages<ul style="list-style-type: none"><li>○ Acceptor for Regular Languages</li><li>○ Regular Expressions</li><li>○ Closure Properties of Regular Languages</li><li>○ Decidable Problems for Regular Languages</li></ul></li><li>• Theory of Context Free Languages<ul style="list-style-type: none"><li>○ Simplification of Context Free Languages</li><li>○ Normal Forms for Context Free Languages</li><li>○ Acceptor for Context Free Languages</li><li>○ Closure Properties of Context Free Languages</li><li>○ Decidable Problems for Regular Languages</li></ul></li><li>• Theory of Context Free and Type Zero Languages<ul style="list-style-type: none"><li>○ Standard Turing Machine</li><li>○ Non-standard Models of Turing Machines</li><li>○ Acceptor for Context sensitive Languages</li></ul></li><li>• Turing Thesis and Computability</li></ul>		
<b>References:</b> [1] P. Linz, <i>Introduction to Formal Languages and Automata</i> , 6 <sup>th</sup> Edition, Jones & Bartlet Learning, 2017. [2] J. E. Hopcroft, R. Motwani, J. D. Ullman, <i>Introduction to Automata Theory, Languages, and Computation</i> , 3 <sup>rd</sup> Edition, Addison-Wiley, 2006. [3] T. A. Sudkamp, <i>Languages and Machines</i> , 3 <sup>rd</sup> Edition, Pearson Education Inc., 2006.		



## 3-1-15 Applied Linear Algebra (CE210)

Applied Linear Algebra (CE210)		
Units	Prerequisites	Corequisites
3	Mathematics II	---
<b>Objectives:</b> The objective of this course is studying topics of linear algebra, its applications and optimization that are extensively used in different fields of computer engineering. This course covers some theoretical topics and also, in simulation exercises, the students become familiar with the relevant software and applications in computer engineering.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Linear Equations in Linear Algebra<ul style="list-style-type: none"><li>○ Linear systems and their solutions</li><li>○ Matrices</li><li>○ Matrix equation</li><li>○ Linear independence</li><li>○ Linear transformations</li></ul></li><li>• Matrix Algebra<ul style="list-style-type: none"><li>○ Matrix operations</li><li>○ Inverse of matrix</li><li>○ Matrix factorization</li><li>○ Determinants</li></ul></li><li>• Vector Spaces<ul style="list-style-type: none"><li>○ Vector spaces and subspaces</li><li>○ Null space</li><li>○ Column space</li><li>○ Bases</li><li>○ Dimension of a vector space</li><li>○ Rank</li><li>○ Change of basis</li></ul></li><li>• Eigenvalues and Eigenvectors<ul style="list-style-type: none"><li>○ Eigenvalues and eigenvectors</li><li>○ Characteristic equation</li><li>○ Diagonalization</li><li>○ Applications</li></ul></li><li>• Orthogonality and Least Squares<ul style="list-style-type: none"><li>○ Inner products</li></ul></li></ul>		



- Orthogonal sets
- The Gram-Schmidt process
- Least squares problems
- Applications
- Singular Value Decomposition
- Principal Component Analysis
- Optimization
  - Vector functions
  - First and second order derivative
  - Introduction to different types of optimization problems
  - Linear programming
  - The simplex algorithm

**References:**

- [1] D. C. Lay, S. R. Lay, and J. J. McDonald, *Linear Algebra and its applications*, 5<sup>th</sup> Edition, Pearson, 2015.
- [2] P. N. Klein, *Coding the Matrix: Linear Algebra through Applications to Computer Science*, Newtonian Press, 2013.
- [3] B. Noble, J. W. Daniel, *Applied Linear Algebra*, 3<sup>rd</sup> Edition, Pearson, 1987.





## 3-1-16 Microprocessor and Assembly Language (CE301)

Microprocessor and Assembly Language (CE310)		
Units	Prerequisites	Corequisites
3	Computer Architecture	Microprocessor and Assembly Language Laboratory
<p><b>Objectives:</b></p> <p>On successful completion of this course students will be familiar with hardware and software aspects of microprocessor- and microcontroller-based systems (using a specific architecture such as AVR or ARM) including their internal architecture and modules, programming in assembly and high level languages, interfacing with memories and Input/outputs, handshaking between microprocessor and peripherals, practical examples and case studies of microcontroller-based digital embedded and IoT systems.</p>		
<p><b>Topics:</b></p> <ul style="list-style-type: none"><li>• Introduction to microprocessors, their history, packaging methods, different kinds of microprocessors including general purpose microprocessors, digital signal processors, microcontrollers, and special purpose processors</li><li>• Understand basic microcontroller architecture modules</li><li>• Registers, control, data and address busses, instruction queue, pipelining, central processing unit, arithmetic logic unit, memory, stack, etc.</li><li>• Brief introduction of manufacturing companies, types of architectures, software development tools, programming and troubleshooting of microcontroller programs</li><li>• Microcontroller assembly language, addressing modes, instruction format, instruction sets, subroutine, macro, directives, expressions</li><li>• System clock and clock options</li><li>• System control and reset, Watchdog timer</li><li>• Input/Output ports in microcontrollers, Interrupts (external interrupts)</li><li>• Timers/counters (counting, waveform generation, PWM, etc)</li><li>• Analog Comparator (AC)</li><li>• Analog to digital convertor (ADC)</li><li>• Power management and sleep modes</li><li>• Serial port and synchronous and asynchronous communication, introduction of USART, familiarity with RS232 and RS485 protocols and daisy chain configuration</li><li>• Memory (SRAM, EEPROM, Flash, etc.), memory mapping and interfacing, address decoding, external memory interface timing</li><li>• Address decoding for Input/output ports, I/O interface timing</li><li>• Peripheral controlling (polling and interrupts), understand the operational principles of hardware support for processor interrupts</li></ul>		



- Practical examples and case studies of microcontroller-based embedded and IoT systems

## References:

- [1] M. M. Homayounpour, F. Hesar, S. A. Asghari, A. Ghanbari, *Microprocessor and Assembly Language*, Sheikh Bahaie Publisher, 2015.
- [2] M. A. Mazidi, S. Naimi, S. Naimi, *The AVR Microcontroller and Embedded Systems using Assembly and C*, Prentice Hall, 2010.
- [3] M. A. Mazidi, D. Causey, R. D. McKinlay, *PIC Microcontroller and Embedded Systems using Assembly and C for PIC18*, Prentice Hall, 2008.
- [4] R. H. Barnett, S. Cox, L. O'Cull, *Embedded C Programming and the Atmel AVR*, Delnmar Cengage Learning Publishing, 2011.
- [5] B. van Dam, *Microcontroller System Engineering, 45 projects for PIC, AVR and ARM*, 2008.
- [6] T. Wilmshurst, *Designing Embedded Systems with PIC Microcontrollers, Principles and Applications*, 2<sup>nd</sup> Edition, Newnes, 2010.
- [7] *Atmel and ARM Microcontrollers' Datasheets*.



## 3-1-17 Microprocessor and Assembly Language Laboratory (CE302)

Microprocessor and Assembly Language Laboratory (CE302)		
Units	Prerequisites	Corequisites
1	---	Microprocessor and Assembly Language
<b>Objectives:</b> By successfully completing this lab, students should be familiar with the issues outlined below in the design and implementation of microprocessor-based and microcontroller-based systems. In order to target the activities of this laboratory and to create interest and enthusiasm for the student, it is recommended that the final project be defined from the beginning and in the first sessions of the laboratory, and its characteristics are such that by the tests students perform during each session, he/she learns to do a part of the final laboratory project.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Familiarity with a simulation software (such as Proteus); Familiarity with a PCB design software (such as Altium software), and ultimately doing a simple electronic project.</li><li>• Familiarity with an assembler and a microcontroller compiler (such as AVR Studio and CodeVision for the AVR family).</li><li>• Providing or constructing a programmer, setting up reset circuit, scheduling fuse bits, producing clock signal for microcontroller</li><li>• Provide a reset interrupt service routine, set up stack pointer, work with ports, polling a pin from a port, generating software latencies, and working with watchdog timer</li><li>• Working with external interrupts and using power saving modes</li><li>• Working with ports, reading the value specified by a four or eight-bit Dip-Switch connected to a port, converting the read value to a BCD No., converting BCD digits to 7-seg codes and displaying the results by two or four 7-seg displays</li><li>• Matrix keyboard (keyboard design and display of digits read from a keyboard by 7-seg displays)</li><li>• Working with LCD (connecting a LCD to microcontroller and displaying information received from the keyboard)</li><li>• Writing and reading data in / from EEPROM microcontroller memory</li><li>• Work with timer/counter, microcontroller in normal operation mode and CTC (blinking with two LEDs connected to two pins of microcontroller that alternately turns on and off).</li><li>• Working simultaneously with two timer/counters (digital frequency meter)</li><li>• Working with timer/counter 2 in PWM mode (adjusting the intensity of a LED illumination or adjusting a small low voltage DC motor speed by PWM wave)</li><li>• Working with microcontroller analogue comparator (switching on and off a LED by increasing or decreasing the voltage of one of the analog comparator input relative to its other input)</li></ul>		



- Working with an analogue to digital convertor (measuring the temperature or making a voltmeter or resistance meter and displaying the measured value on a LCD)
- Working with the microcontroller USART interface (communicating between two microcontrollers or communicating between a microcontroller and a computer via RS232 communication using the USART interface on the microcontroller side and a USART Serial Connection .NET Component terminal program on the computer side)
- Working with the SPI interface of the microcontroller and connecting two microcontrollers using SPI interface; writing and reading data on the SD RAM (Optional)
- Working with the TWI interface of the microcontroller and connecting two microcontrollers using the TWI interface, working with the real time clock (RTC) chip and displaying the actual time on the LCD (optional)

## References:

- [1] M. M. Homayounpour, F. Hesar, S. A. Asghari, A. Ghanbari, *Microprocessor and Assembly Language*, Sheikh Bahaie Publisher, 2015.
- [2] M. A. Mazidi, S. Naimi, S. Naimi, *The AVR Microcontroller and Embedded Systems using Assembly and C*, Prentice Hall, 2010.
- [3] M. A. Mazidi, D. Causey, R. D. McKinlay, *PIC Microcontroller and Embedded Systems using Assembly and C for PIC18*, Prentice Hall, 2008.
- [4] R. H. Barnett, S. Cox, L. O'Cull, *Embedded C Programming and the Atmel AVR*, Delmar Cengage Learning Publishing, 2011.
- [5] B. van Dam, *Microcontroller System Engineering, 45 projects for PIC, AVR and ARM*, 2008.
- [6] T. Wilmshurst, *Designing Embedded Systems with PIC Microcontrollers, Principles and Applications*, 2nd Edition, Newnes, 2010.
- [7] *Atmel and ARM Microcontrollers' Datasheets*.



## 3-1-18 Operating Systems (CE303)

Operating Systems (CE303)		
Units	Prerequisites	Corequisites
3	Computer Architecture	Operating Systems Laboratory
<b>Objectives:</b> The main objective of this course is to introduce structure and organization of operating systems. Students are being familiar with basic concepts of operations systems including internal structure, interrupts, system calls, processor internal capability, multicores and multi processors issues, process synchronization, CPU scheduling, memory management, protection and security issues, in this course.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction and operating-system structures</li><li>• Process management</li><li>• Threads and concurrency</li><li>• CPU scheduling</li><li>• Process synchronization, tools and examples</li><li>• Deadlocks, livelock and starvations</li><li>• Main memory management</li><li>• Virtual memory management</li><li>• Mass-storage structure</li><li>• I/O systems</li><li>• File-system interface</li><li>• Protection and security</li><li>• Virtualization and virtual machines</li></ul>		
<b>References:</b> [1] A. Silberschatz, P.B. Galvin, G. Gagne, <i>Operating System Concepts</i> , 9 <sup>th</sup> Edition, Wiley, 2013. [2] W. Stallings, <i>Operating Systems: Internal and Design Principles</i> , 9 <sup>th</sup> Edition, Pearson, 2015. [3] A.S. Tanenbaum, <i>Modern Operating Systems</i> , 4 <sup>th</sup> Edition, Pearson, 2014.		



## 3-1-19 Operating Systems Laboratory (CE304)

Operating Systems Laboratory (CE304)		
Units	Prerequisites	Corequisites
3	---	Operating Systems
<b>Objectives:</b> This course lab aimed to test and to do experiments of operating systems concepts. The course is divided into two parts: 1) an ability to work with well-known open-source operating systems like Linux and have kernel module programming (practical skills) and 2) an ability to implement operating system concepts and have experience what are problems and how to solve them.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Linux introduction<ul style="list-style-type: none"><li>○ Definition and Installing</li><li>○ Filesystem and file management</li><li>○ Protection and permission</li></ul></li><li>• Kernel module programming<ul style="list-style-type: none"><li>○ Create a kernel module</li><li>○ Load and remove a kernel module</li><li>○ Kernel structure</li></ul></li><li>• Bash scripting<ul style="list-style-type: none"><li>○ Variable initialization and system variables</li><li>○ Condition statement, case statement, loop template</li></ul></li><li>• Thread and process</li><li>• Inter-process communication</li><li>• Synchronization</li><li>• Deadlock</li><li>• Scheduling</li><li>• OpenMP</li><li>• Final project</li></ul>		
<b>References:</b> [1] M. Garrels, <i>Introduction to Linux, a Beginner's Guide</i> , 2010. [2] M. Mitchell, J. Oldham, and A. Samuel, <i>Advanced Linux Programming</i> , 2001. [3] A. Silberschatz, P.B. Galvin, G. Gagne, <i>Operating System Concepts</i> , 9 <sup>th</sup> Edition, Wiley, 2013. [4] W. Stallings, <i>Operating Systems: Internal and Design Principles</i> , 9 <sup>th</sup> Edition, Pearson, 2015. [5] A.S. Tanenbaum, <i>Modern Operating Systems</i> , 4 <sup>th</sup> Edition, Pearson, 2014		



## 3-1-20 Computer Networks (CE305)

Computer Networks (CE305)		
Units	Prerequisites	Corequisites
3	Engineering Probability and Statistics, Computer Architecture	Operating Systems, Computer Networks Laboratory
<b>Objectives:</b> This course provides an introduction to fundamental concepts in the design, implementation and performance evaluation of computer networks. In this course, students get familiar with computer networks architecture, services, and applications. This course studies the application layer, the transport layer, the network layer, and the data link layer protocols with emphasis on the Internet and TCP/IP model.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Review of Computer Networks Services</li><li>○ Definition of Computer Networks, Service, Quality of Service, and Protocol</li></ul></li><li>• The Internet and Its Components<ul style="list-style-type: none"><li>○ The Edge and Core Networks</li><li>○ Client-Server Model</li><li>○ Access Networks</li><li>○ Circuit Switching and Packet Switching Networks</li><li>○ Quality of Service Parameter in Packet Switch Networks</li></ul></li><li>• Layered Architecture of Computer Networks<ul style="list-style-type: none"><li>○ OSI Reference Model</li><li>○ Protocols and Services</li><li>○ Connection-Oriented and Connectionless service Models</li><li>○ Segmentation and Reassembly</li><li>○ Multiplexing and Demultiplexing</li><li>○ TCP/IP Model</li></ul></li><li>• Application Layer<ul style="list-style-type: none"><li>○ The Web and HTTP</li><li>○ FTP Protocol</li><li>○ Electronic Mail and SMTP Protocols</li><li>○ The Internet's Directory Service and DNS Protocol</li><li>○ Peer-to-Peer Applications</li><li>○ Socket Programming</li></ul></li></ul>		



- Transport Layer
  - Introduction and Transport-Layer Services
  - Connectionless Service of Transport Layer and UDP Protocol
  - Principles of Reliable Data Transfer
  - Connection-Oriented Service of Transport Layer and TCP Protocol
  - Principles of Congestion Control
  - TCP Congestion Control
- The Network Layer
  - Introduction and Network Layer Services (Forwarding and Routing)
  - Virtual Circuit and Datagram Networks
  - Router Architecture
  - Traffic Management in Packet Switch Networks
  - The Internet Protocols (IPv4, IPv6, ICMP, ARP Protocols)
  - DHCP and Mobile IP Protocols
  - Routing Algorithms (Link-State and Distance-Vector Routing Algorithms)
  - Routing Protocols in the Internet
- The Link Layer and Local Area Networks
  - Introduction and Data Link layer Services
  - Introduction to Error-Detection and Error-Correction Techniques
  - Introduction to Medium Access Control Techniques, Ethernet and Wireless LANs

## References:

- [1] J. F. Kurose, K. W. Ross, *Computer Networking: A Top-Down Approach*, 7<sup>th</sup> Edition, Pearson, 2016.
- [2] A. Leon-Garcia, I. Widjaja, *Communication Networks*, 2<sup>nd</sup> Edition, McGraw-Hill, 2003.
- [3] A. S. Tanenbaum, *Computer Networks*, 5<sup>th</sup> Edition, Pearson, 2010.
- [4] B. A. Forouzan and DeAnza College, *Data Communications and Networking*, 5<sup>th</sup> Edition, McGraw-Hill, 2012.
- [5] W. Stallings, *Data and Computer Communications*, 10<sup>th</sup> Edition, Pearson, 2014.





## 3-1-21 Computer Networks Laboratory (CE306)

Computer Networks Laboratory (CE306)		
Units	Prerequisites	Corequisites
1	---	Computer Networks
<b>Objectives:</b> The goal of the computer networks laboratory is to create the correct understanding of the basic concepts of computer networks. In this laboratory, the students will be familiar with tools and equipment needed to design, implement, and management of computer networks. They also perform some practical experiments based on theoretical concepts.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction</li><li>• Simple tools for network management and performing the necessary experiments</li><li>• Packet Capturing tools and performing the necessary experiments</li><li>• Simulation and emulation tools of network and its equipment, and performing the necessary experiments</li><li>• Perform experiments to understand the application layer protocols</li><li>• Perform experiments to understand the transport layer protocols</li><li>• Perform experiments to understand the packet forwarding function</li><li>• Perform experiments to understand static and dynamic routing, and routing protocols</li><li>• Perform experiments for familiarity with wireless LANs</li><li>• Perform experiments for familiarity with Ethernet</li><li>• Perform experiments for familiarity with Ethernet network cabling</li></ul>		
<b>References:</b> [1] J. F. Kurose, K. W. Ross, <i>Computer Networking: A Top-Down Approach</i> , 7 <sup>th</sup> Edition, Pearson, 2016.		



## 3-1-21 Software Engineering I (CE307)

Software Engineering I (CE307)		
Units	Prerequisites	Corequisites
3	Advanced Programming	---
<b>Objectives:</b> The purpose of this course is to address engineering issues that must be followed in all stages of software development. In this course, students will get acquainted with the activities and tools needed to produce a software product.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ System Analysis and Design and Software Engineering</li><li>○ Components of information systems and dimensions of system analysis and design and software lifecycle</li></ul></li><li>• Software development processes<ul style="list-style-type: none"><li>○ Guiding principles in all software development activities</li><li>○ Structured processes</li><li>○ Agile processes</li></ul></li><li>• System analysis<ul style="list-style-type: none"><li>○ Requirements engineering</li><li>○ Scenario based modeling and use cases</li><li>○ Data analysis and modeling</li><li>○ Process analysis and modeling</li><li>○ Object-oriented analysis and modeling</li><li>○ Documentation using UML</li><li>○ Feasibility studies based on requirements engineering</li></ul></li><li>• System design and implementation<ul style="list-style-type: none"><li>○ Different design approaches</li><li>○ System architecture</li><li>○ Model-based structured design</li><li>○ Object-oriented design</li><li>○ Software development</li></ul></li><li>• Umbrella activities<ul style="list-style-type: none"><li>○ Different approaches and stages of design and implementation of software tests</li><li>○ Preliminary introduction to Project Management</li></ul></li><li>• Configuration and documentation management</li></ul>		



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**References:**

- [1] R. S. Pressman, *Software Engineering: A Practitioner's Approach*, McGraw-Hill, 7<sup>th</sup> Edition, 2011.
- [2] J. Whitten, L. Bentley, *Systems Analysis and Design Methods*, McGraw-Hill, 7<sup>th</sup> Edition, 2005.
- [3] M. Fowler, *UML Distilled*, 3<sup>rd</sup> Edition, OMG Standard Group.



## 3-1-23 Research and Presentation Methods (CE308)

Research and Presentation Methods (CE308)		
Units	Prerequisites	Corequisites
2	Technical English	---
<b>Objectives:</b> The purpose of this course is to develop students' skills for conducting research and presenting the results effectively. Steps in doing research are discussed and appropriate presentation of the results in both oral and written forms is taught. Each student practices the lessons with selecting a topic and doing different taught steps during the semester. Students get familiar with software tools aiding in research and in written and oral presentations.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Research in engineering</li><li>○ Good presentation: factors and impact</li></ul></li><li>• Basic concepts<ul style="list-style-type: none"><li>○ Ethics in research</li><li>○ Research question and goals</li><li>○ Mind map</li><li>○ Fish bone diagram</li><li>○ Research methodology in engineering</li><li>○ Scientific methodology</li><li>○ Modeling and simulation</li></ul></li><li>• Research proposal<ul style="list-style-type: none"><li>○ Research topic and title</li><li>○ Preparing a research proposal</li><li>○ Undergraduate and postgraduate thesis proposals</li><li>○ Scheduling and budget estimation</li></ul></li><li>• Conducting research<ul style="list-style-type: none"><li>○ Acquiring information from the Internet</li><li>○ Variety of resources</li><li>○ Resource validation and selection,</li><li>○ Study of the subject background</li><li>○ Paper reading and note-taking,</li><li>○ Critical points in conducting empirical research</li><li>○ Writing the first draft of the report</li><li>○ Introducing software tools for research</li></ul></li></ul>		



- Oral Presentation
  - Organizing and scheduling oral presentations,
  - Presentation plan
  - Critical points to observe before and during presentation
  - Slide preparation
  - Software tools for oral presentation
- Special types of oral presentation
  - Seminars
  - Theses defense
  - Conference presentations
  - Interview tips
- Written presentation
  - Standard elements of an engineering report
  - Reference writing styles and citation methods
  - Plagiarism, writing quality
  - Editing the report
  - Software tools for written presentation
- Special types of written presentation
  - Seminars
  - Thesis report
  - Internship report
  - Papers
  - Resume

## References:

- [1] R. Safabakhsh, *Research and Presentation in Engineering (in Farsi)*, AUT Press, 2013..
- [2] A. Aliahmadi, *A Comprehensive Description of Research Methods (in Farsi)*, Tolide Danesh Pub., 2007.
- [3] G. Khaki, *Research Methods with Thesis Writing Approach (in Farsi)*, Baztab Pub., 2003.
- [4] S. Taiebi, M.R. Maleki, and B. Delgoshae, *Preparation of Thesis, Dissertation, Research Proposals, and Scientific Papers (in Farsi)*, Ferdows Pub., 2009.



## 3-2 Packages Courses

### 3-2-1 Algorithm Design (CE221)

Algorithm Design (CE221)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms	---
<b>Objectives:</b> The main goal of this course is teaching the basic concepts and typical methods of design and analysis of algorithms. In this course, students learn how to analyze a given problem and develop algorithms to solve it. Then, they learn how to analyze and compare the time complexity of the algorithms and select the best one. In this course, also the basic algorithms for solving some well-known problems are introduced.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Concept of algorithm</li><li>○ Motivation of studying design and analysis of algorithms</li><li>○ Introducing some basic concepts</li><li>○ Asymptotic analysis</li><li>○ Amortized analysis</li></ul></li><li>• Correctness proof of algorithms<ul style="list-style-type: none"><li>○ Induction</li><li>○ Loop invariants</li></ul></li><li>• Divide and conquer<ul style="list-style-type: none"><li>○ Merge sort</li><li>○ Quick sort</li><li>○ Strassen approach for matrix multiplication</li></ul></li><li>• Dynamic Programming<ul style="list-style-type: none"><li>○ Longest Common Subsequence Problem</li><li>○ Optimal Binary Search Tree</li><li>○ Matrix Multiplication Problem</li></ul></li><li>• Greedy Approach<ul style="list-style-type: none"><li>○ Task Scheduling problem</li><li>○ Activity Selection problem</li></ul></li><li>• Backtracking and Branch and Bound<ul style="list-style-type: none"><li>○ n-Queens problem</li></ul></li></ul>		



- Graph coloring problem
- Knapsack Problem
- Graph Algorithms
  - Shortest path algorithms (Dijkstra, Floyd)
  - Minimum spanning tree algorithms (Prim, Kruskal)
  - Max Flow algorithms
- Computation complexity
  - Introduction to classes of P, NP, NP-complete, NP-hard

**References:**

- [1] T. Cormen, C. Leiserson, R. Rivest, C. Stein, *Introduction to Algorithms*, MIT Press, 3rd ed., 2009.
- [2] R. Neopolitan, *Foundation of Algorithms*, Jones & Bartlett Learning, 2015.
- [3] E. Horowitz, S. Sahni, and S. Rajasekaran, *Fundamentals of computer algorithms*, Galgotia Pub., 2008.



## 3-2-2 Signal and Systems (CE222)

Signal and Systems (CE222)		
Units	Prerequisites	Corequisites
3	Differential Equations	---
<b>Objectives:</b> This course introduces the methods of description and analysis for continuous-time and discrete-time signals and (linear time-invariant) systems, including time domain analysis using convolution, frequency domain analysis using the Fourier series and the Fourier transform, the Laplace transform and the Z-transform. Students will also learn examples of applications of signal and system analysis methods to communications and control systems.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction and Preliminary Concepts<ul style="list-style-type: none"><li>○ Discrete-Time and Continuous-Time Signals</li><li>○ Systems Types</li><li>○ Basic Signals</li><li>○ Systems Properties</li></ul></li><li>• Linear Time-Invariant Systems<ul style="list-style-type: none"><li>○ Continuous and Discrete Convolution</li><li>○ Convolution Properties</li><li>○ Impulse Response and Its Properties</li><li>○ System Block Diagram</li></ul></li><li>• Continuous-Time Fourier Series</li><li>• Continuous-Time Fourier Transform</li><li>• Applications of Continuous-Time Fourier Transform<ul style="list-style-type: none"><li>○ Sampling</li><li>○ Filtering</li><li>○ Communication Systems</li></ul></li><li>• Discrete-Time Fourier Series</li><li>• Discrete-Time Fourier Transform</li><li>• Z-Transform</li><li>• Laplace Transform</li></ul>		
<b>References:</b> [1] A.V. Oppenheim, A.S. Willsky, S.H. Nawab, <i>Signals and Systems</i> , 2 <sup>nd</sup> Edition, Prentice-Hall, 1997.		





## 3-2-3 Principles of Database Design (CE231)

Principles of Database Design (CE231)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms	---
<b>Objectives:</b> Database management has evolved from a specialized computer application to a central component of a modern computing environment, and, as a result, knowledge about database systems has become an essential part of an education in computer science. In this course, the fundamental concepts of database management including aspects of database design, database languages, and database-system implementation will be presented.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to Database</li><li>• Introduction to the Relational Model</li><li>• Formal Languages</li><li>• Introduction to SQL</li><li>• Intermediate and Advanced SQL</li><li>• Database Design and the Entity Relationship Model</li><li>• Normalization (1st, 2nd, 3rd and BCNF)</li><li>• Higher Order Normalization</li><li>• Advanced Topics in Database<ul style="list-style-type: none"><li>○ Selecting at least two of the following topics: Object-based Data Models, XML, NoSQL, OLAP, Data Warehouse</li></ul></li></ul>		
<b>References:</b> [1] A. Silberschatz, H. F. Korth, S. Sudarshan: <i>Database System Concepts</i> , 6 <sup>th</sup> Edition, McGraw-Hill, 2010. [2] C. J. Date, <i>Introduction to Database Systems</i> , 8 <sup>th</sup> Edition, Addison-Wesley, 2003		



## 3-2-4 Compiler Design (CE232)

Compiler Design (CE232)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms	---
<b>Objectives:</b> The objective of this course is to learn the principles, techniques and tools for constructing a compiler. In this course, after introduction of the basic concepts of compiler design, lexical and syntax analyses are discussed. After that, run-time environment as well as intermediate code generation concepts are explained.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to compiler</li><li>• Lexical Analysis</li><li>• Syntax Analysis<ul style="list-style-type: none"><li>○ Top down parser</li><li>○ bottom up parser</li></ul></li><li>• Symbol table and Run-Time Environment</li><li>• Intermediate Code Generation</li><li>• Project: Design and implementation of a front end of a compiler using Compiler-Compilers</li></ul>		
<b>References:</b> [1] A. Vlahos, R. Sethi, J. D. Ullman, <i>Compilers: Principles, Techniques, and Tolls</i> , 2 <sup>nd</sup> Edition, Addison-Wesley, 2007. [2] D. Grune, H. Bal, C. Jacobs, K. Langendoen, <i>Modern Compiler Design</i> , JohnWiley & Sons, 2000.		



## 3-2-5 Programming Languages (CE233)

Programming Languages (CE233)		
Units	Prerequisites	Corequisites
3	Theory of Languages and Machines	---
<b>Objectives:</b> This course acquaints students with the concepts and constituents of programming languages. It also surveys the constructs and capabilities typically found in modern programming languages with attention to design trade-offs and implementation considerations. The course also develops the skills necessary to learn new programming languages quickly. Learning the capabilities of different classes of programming languages as well as familiarizing the students with instances of each class is among the objectives of this course.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Programming Languages</li><li>○ Computational Models</li><li>○ Classes of Programming Languages</li><li>○ Language and Computability</li></ul></li><li>• Syntax<ul style="list-style-type: none"><li>○ Formal Syntax</li><li>○ Concrete and Abstract Syntax</li></ul></li><li>• Dynamic Semantics<ul style="list-style-type: none"><li>○ Operational</li><li>○ Denotational</li><li>○ Axiomatic Semantics</li></ul></li><li>• Lambda Calculus<ul style="list-style-type: none"><li>○ Syntax</li><li>○ Semantics</li><li>○ Recursive Functions</li></ul></li><li>• Functional Languages<ul style="list-style-type: none"><li>○ Lisp</li><li>○ ML</li><li>○ Haskell</li></ul></li><li>• Static Semantics<ul style="list-style-type: none"><li>○ Type Systems</li><li>○ Safe Languages</li></ul></li></ul>		



- Type Safety
- Typed Lambda Calculus
- Type Inference
- Scope, Functions, and Memory Management
  - Block-Structured Languages
  - Activation Records
  - Closures
- Control in Sequential Languages
  - Structured Control
  - Exceptions
  - Continuations
  - Functions and Evaluation Order
- Modularity, Abstraction, and Object-Oriented Programming
  - Structured Programming,
  - Modules
  - Concepts in Object-Oriented Languages
  - Simula, Smalltalk, C++, and Java
- Concurrent and Distributed Programming
  - Basic Concepts in Concurrency
  - Actor Model
  - Core Calculus of Concurrency
  - Concurrent ML
  - Concurrency in Java
- Logic Programming
  - Concepts of First-Order Logic
  - Resolution
  - Prolog

## References:

- [1] J. C. Mitchell, *Concepts in Programming Languages*, Cambridge University Press, 2002.
- [2] R. W. Sebesta, *Concepts of Programming Languages*, 11<sup>th</sup> Edition, Pearson, 2015.
- [3] B. C. Pierce, *Types and Programming Languages*, MIT Press, 2002.



## 3-2-6 Interface Circuits Design (CE241)

Interface Circuits Design (CE241)		
Units	Prerequisites	Corequisites
3	Microprocessor and Assembly Language	---
<b>Objectives:</b> The objectives of this course is <ul style="list-style-type: none"><li>To provide basic concepts and principles of hardware and software interfaces without a focus on a particular architecture or processor</li><li>To learn the main principles of commonly used interface standards and protocols</li><li>To develop skills to design and implement interface circuits</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>Introduction to interfaces<ul style="list-style-type: none"><li>Types</li><li>Hardware/Software</li><li>Analog/Digital</li></ul></li><li>Buses<ul style="list-style-type: none"><li>Theoretical aspects</li><li>Different types</li><li>Applications</li><li>Comparison</li></ul></li><li>Different bus architectures<ul style="list-style-type: none"><li>CPU-Memory</li><li>I/O</li><li>DMA</li><li>etc.</li></ul></li><li>Universal Serial Bus (USB)</li><li>IEEE 1394 Standard (Firewire)</li><li>AMBA AHB/APB<ul style="list-style-type: none"><li>Overall architecture and design with a focus on FPGA</li></ul></li><li>PCI family of interfaces</li><li>Storage device interfaces (SATA)</li><li>Memory chip interfaces (DDR)</li><li>Multimedia interfaces (HDMI)</li><li>Debugging interfaces (SPI and I2C)</li></ul>		



- IEEE 1149.1 Standard (JTAG)
- Device drivers (firmware interfaces)
- Motors (stepper, servo, DC) and their drivers
- Sensors and actuators
  - Theoretical aspects
  - Design and analysis

**References:**

No single textbook covers all of the material presented in this course. The main source of the course will be the text of standards and protocols. Only the last topic in the course will be based on Chapter 7 of the following textbook:

- [1] E. Lee and S. Seshia, *Introduction to Embedded Systems, A Cyber-Physical Systems Approach*, 2<sup>nd</sup> Edition, 2015.



## 3-2-7 Programmable Digital Systems Design (CE242)

Programmable Digital Systems Design (CE242)		
Units	Prerequisites	Corequisites
3	Computer Architecture	---
<b>Objectives:</b> The objectives of this course are: <ul style="list-style-type: none"><li>• To develop skills in design, verification, debugging and optimization of large digital circuits and systems</li><li>• To develop skills in describing digital circuits and systems in hardware description languages</li><li>• To enable students to implement a complete system on a development board based on programmable devices</li><li>• To gain familiarity with architectures of programmable logic devices</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Digital systems design flow</li><li>• Hardware description: basic concepts and applications</li><li>• Levels of abstraction<ul style="list-style-type: none"><li>○ Behavioral level</li><li>○ Register-transfer level</li><li>○ Gate level</li></ul></li><li>• Describing hardware in one of hardware description languages</li><li>• Synthesis and synthesizable description</li><li>• Designing in register-transfer level; control path/data path</li><li>• Verification and simulation</li><li>• Placement and routing</li><li>• Digital systems analysis<ul style="list-style-type: none"><li>○ Timing analysis</li><li>○ Power analysis)</li></ul></li><li>• Design techniques for optimizing designs and design process<ul style="list-style-type: none"><li>○ Pipelining</li><li>○ Retiming</li><li>○ Ad-hoc techniques)</li></ul></li><li>• Introduction to hardware/software co-design</li><li>• Commercial programmable logic devices and their architectures</li></ul>		



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**References:**

- [1] P. Simpson, *FPGA Design: Best Practices for Team-based Reuse*, Springer, 2015.
- [2] P. Chu, *RTL Hardware Design Using VHDL: Coding for Efficiency, Portability, and Scalability*, Wiley, 2006.
- [3] P. Wilson, *Design Recipes for FPGAs*, 2<sup>nd</sup> Edition, Elsevier Science & Technology, 2015.
- [4] C. Maxfield, *The Design Warrior's Guide to FPGA*, Elsevier, 2004





## 3-2-8 Digital Electronics (CE243)

Digital Electronics (CE243)		
Units	Prerequisites	Corequisites
3	Electrical and Electronic Circuits	---

### Objectives:

Digital Electronics course provides basic concepts and principles of semiconductor devices with an emphasis on Metal Oxide Semiconductor Field Effect Transistors (MOSFET) along with an in-depth knowledge of CMOS semiconductor devices. In addition, it provides skills to model, analyze, and simulate digital electronic circuits at logic and transistor levels.

### Topics:

- Introduction to MOSFETs
  - Behavior and characteristics
  - Shockley model
  - I-V curves
  - A brief overview of CMOS fabrication process
- NMOS inverters
  - Circuit models
  - Advantages and disadvantages
  - Comparison between resistive load vs. active load
- CMOS inverter
  - Circuit model
  - DC characteristics
  - Delay and timing behavior
  - Parasitic capacitances
  - Power consumption
- CMOS complex gates
  - Theoretical and practical analysis
- CMOS buffers and drivers
  - Theoretical and practical analysis
- CMOS tristate and Schmitt trigger inverters
- Pass transistors and transmission gates
- CMOS sequential circuits
- Dynamic CMOS, Differential CMOS circuits
- Introduction to BiCMOS technology



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**References:**

- [1] M. Sedighi, A. Valizadeh, *Digital Electronic Devices*, Amirkabir University Press, 2016.
- [2] N. Weste, D. Harris, *CMOS VLSI Design: A Circuits and Systems Perspective*, 4<sup>th</sup> Edition, Addison-Wesley Press, 2011.



## 3-2-9 Principles and Applications of Artificial Intelligence (CE251)

Principles and Applications of Artificial Intelligence (CE251)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms	Applied Linear Algebra
<b>Objectives:</b> The goal of this course is to familiarize students with the basics, methods and applications of artificial intelligence. Artificial intelligence definitions, rationality concept, task environment properties and agent architectures are introduced in the first part of the course. Students learn how to model real world problems as search/optimization problem and how to solve them using informed and uninformed search strategies. Propositional and first order logic are also covered as knowledge representation and inference tools. A number of artificial intelligence applications including text processing, visual perception, and robotics are also introduced.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction</li><li>• Intelligent Agents</li><li>• Problem solving with search</li><li>• Adversarial search</li><li>• Constraint satisfaction problems</li><li>• Non-classical search algorithms</li><li>• Text, speech and natural processing</li><li>• Visual perception</li><li>• Logical Agents</li><li>• First-order logic</li><li>• Knowledge representation</li><li>• Text, speech and natural processing</li><li>• Visual Perception</li><li>• Robotics</li></ul>		
<b>References:</b> [1] S. J. Russel, P. Norvig, <i>Artificial Intelligence, A Modern Approach</i> , 3 <sup>rd</sup> Edition, Pearson Education, 2009. [2] A. Konar, <i>Computational Intelligence: Principles, Techniques and Applications</i> . Springer, 2007.		



## 3-2-10 Web Programming (CE261)

Web Programming (CE261)		
Units	Prerequisites	Corequisites
3	Computer Networks	---
<b>Objectives:</b> As the web is the main application on the Internet, the objective of this course is to learn the concepts, technologies, tools and programming languages used in both client and server side of web. In client side, students gradually learn from static page development to complex interactive pages. In server side, the main requirements of a typical application including input data handling, session management, and DB access are discussed.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• HTTP</li><li>• HTML</li><li>• CSS</li><li>• XML &amp; JSON</li><li>• JavaScript</li><li>• HTML5</li><li>• Ajax</li><li>• A client side framework (e.g. Vue)</li><li>• A server side programming language (e.g. PHP)</li><li>• Input processing</li><li>• Session management</li><li>• Database Access and ORM</li><li>• Concurrency Control</li><li>• Error Handling &amp; Debugging</li><li>• Security</li><li>• Web Application Architectures</li></ul>		
<b>References:</b> [1] J. Duckett, <i>HTML &amp; CSS: Design and Build Websites</i> , Wiley, 2011. [2] J. Duckett, <i>JavaScript and jQuery: Interactive Front-End Web Development</i> , Wiley, 2014. [3] K. Yank, <i>PHP &amp; MySQL: Novice to Ninja</i> , 6 <sup>th</sup> Edition, SitePoint, 2017.		



## 3-2-11 Software Engineering II (CE331)

Software Engineering II (CE331)		
Units	Prerequisites	Corequisites
3	Software Engineering I	---
<b>Objectives:</b> This is the second of two courses in the Software Engineering. While Software Engineering I focuses on software production topics such as processes, requirements and architectures, Software Engineering II focuses on how the quality software will be. Much of the content is organized around the software processes, software planning, risk, estimation, and architecture and detailed design.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction</li><li>• WebApp Design</li><li>• MobileApp Design</li><li>• Quality Concepts</li><li>• Review Techniques</li><li>• Software Quality Assurance</li><li>• Software Testing Strategies</li><li>• Testing Conventional Applications</li><li>• Testing Object-Oriented Applications</li><li>• Testing Web Applications</li><li>• Testing Mobile Apps</li><li>• Product Metrics</li><li>• Process and Project Metrics</li><li>• Estimation for Software Projects</li><li>• Risk management</li></ul>		
<b>References:</b> [1] R. Pressman, <i>Software Engineering -- A Practitioner's Approach</i> , 8 <sup>th</sup> Edition, McGraw Hill, 2014. [2] I. Sommerville, <i>Software Engineering</i> , 9 <sup>th</sup> Edition, Addison-Wesley, 2011.		



## 3-2-12 Embedded and Real-Time Systems (CE341)

Embedded and Real-Time Systems (CE341)		
Units	Prerequisites	Corequisites
3	Operating Systems, Microprocessor and Assembly Language	---

### Objectives:

Embedded systems play a decisive role in various applications including healthcare, transportation, traffic control, financial and information systems, avionics, and home appliance. This course focuses on design, implementation, and analysis of embedded and real-time systems. Hardware and software components of an embedded system, task scheduling approaches, process and resource management in real-time systems, and programming techniques are covered in this course.

### Topics:

- Preliminaries and basic concepts
  - Applications
  - Trends
  - Characteristics
  - Challenges
  - Computing paradigm
- Hardware components
  - Processors
  - Memory structure
  - Sensors and actuators
  - Communication
- State-machine model
  - System design
  - Interrupts
- Communications in embedded systems
  - Network protocols
  - Distributed embedded systems
  - Integrity in distributed embedded systems
- Operating system and embedded software
  - Real-time scheduling
  - Compilers and Low-level programming
  - Software optimizations
  - Internet-of-Things



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**References:**

- [1] E. A. Lee, S. A. Seshia, *Introduction to Embedded Systems – A Cyber-Physical System Approach*. MIT Press, 2016.
- [2] P. Marwedel, *Embedded System Design (Embedded System Foundation of Cyber-Physical System)*, Springer, 2011.
- [3] R. Love, *Linux Kernel Development*, 2<sup>nd</sup> Edition, Novell, 2005.
- [4] S. Seiwert, *Real-Time Embedded System and Components*, Thomson Learning Inc, Charles River Media, 2007.



## 3-2-13 Multi-Processor Programming (CE342)

Multi-Processor Programming (CE342)		
Units	Prerequisites	Corequisites
3	Operating Systems	---
<b>Objectives:</b> The goal of this course is to give students the skills to design, implement, debug and optimize multithreaded programs, as well as vectorized programs and GPU codes. Students will learn the basics of multicore and GPU architectures and their memory models. They also learn how to profile a given application, identify its performance bottlenecks and how to resolve them, and gain multicore programming skills.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to parallel systems architecture and memory models including shared memory and distributed memory models</li><li>• Introduction to multithreaded programming, programming models and related programming languages</li><li>• Introduction to vector processing, SIMD, SSE, AVX, and how to write a vectorized program</li><li>• Multithreaded and vectorized programming using multicore programming languages (OpenMP)</li><li>• Introduction to thread synchronization methods, lock, barrier, etc.</li><li>• Practical multithreaded applications (matrix operation, sorting, etc.)</li><li>• Introduction to GPU architecture and GPU memory hierarchy</li><li>• Introduction to multithreaded programming for general purpose application development on GPUs</li><li>• Implementing general purpose applications on GPU using CUDA</li><li>• Practical GPU applications (matrix operation, n-body simulation, etc.)</li></ul>		
<b>References:</b> [1] T. Rauber, G. R�ungerParallel, <i>Programming for Multicore and Cluster Systems</i> , Springer, 2013. [2] N. Wilt, <i>The CUDA Handbook: A Comprehensive Guide to GPU Programming</i> , Addison-Wesley, 2013. [3] D. Kirk, <i>Programming Massively Parallel Processors: A Hands-on Approach</i> , Elsevier, 2010. [4] J. Sanders, E. Kandrot, <i>CUDA by Example: An Introduction to General-Purpose GPU Programming</i> , Addison-Wesley, 2010.		





## 3-2-14 Foundations of Computational Intelligence (CE351)

Foundations of Computational Intelligence (CE351)		
Units	Prerequisites	Corequisites
3	Algorithm Design	---
<b>Objectives:</b> This course is designed to familiarize the students with fundamental concepts and principles of intelligent methods for data analysis and engineering problem solving, such as fuzzy methods, evolutionary techniques, and neural networks. To achieve this goal, students get hands-on experience with software tools related to these areas.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to Computational Intelligence</li><li>• Fuzzy Methods<ul style="list-style-type: none"><li>○ Introduction, Basic fuzzy set theory (Membership functions, Fuzzy operators)</li><li>○ Fuzzy relations and fuzzy logic inference (Fuzzy rules, Extension Principle)</li><li>○ Fuzzy Knowledge-based Systems</li></ul></li><li>• Evolutionary Methods<ul style="list-style-type: none"><li>○ Introduction, Genetic algorithm (Representation, Crossover, Mutation, Selection)</li><li>○ Evolution strategies (Adaptation, Self-adaptation, Representation, Crossover, Mutation, Selection)</li><li>○ Particle swarm optimization (Global best, Local best, Inertia weight)</li><li>○ Ant Algorithms (Ant Systems, Ant Colony, Max-Min Ant)</li></ul></li><li>• Neural Networks<ul style="list-style-type: none"><li>○ Introduction,</li><li>○ Artificial neurons (Activation Function, Learning, Perceptron, Adaline)</li><li>○ Supervised neural networks (Feedforward Neural Networks)</li><li>○ Unsupervised neural networks (Self-organizing feature maps, Learning Vector Quantization networks)</li></ul></li><li>• Hybrid CI Algorithms</li></ul>		
<b>References:</b> [1] J. M. Keller, D. Liu and D. B. Fogel, <i>Fundamentals of Computational Intelligence: Neural Networks, Fuzzy Systems and Evolutionary Computation</i> , Wiley, 2016. [2] P. Engelbrecht, <i>Computational Intelligence: An Introduction</i> , Wiley, 2007. [3] Konar, <i>Computational Intelligence: Principles, Techniques and Applications</i> , Springer, 2007.		



## 3-2-15 Introduction to Robotics (CE352)

Introduction to Robotics (CE352)		
Units	Prerequisites	Corequisites
3	Signal and Systems	---
<b>Objectives:</b> The purpose of this course is to introduce basics of mechanism, design, planning, programming and intelligent control of robot systems. The main focus is on different aspect of mobile robots including, kinematics, sensors, actuators, perception, localization, motion planning and navigation. This course is a laboratory based course which students practice via real and simulated robots.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to mobile robotics and robotic manipulation</li><li>• Robot simulators and mobile robot design</li><li>• Spatial descriptions, homogeneous transform, and mobile robot kinematics</li><li>• Robot sensors, measurement and calibration</li><li>• Actuators and drives, DC motors, stepper motors, servomotors, gears, pulse width modulation (PWM)</li><li>• Robot Control basics and principles and PID controllers</li><li>• Robot perception, localization, planning, and navigation</li><li>• Introduction to Kalman filter, program a robot using ROS</li></ul>		
<b>References:</b> [1] R. Siegwart, I. Nourbakhsh, D. Scaramuzza, <i>Introduction to Autonomous Mobile Robots</i> , The MIT Press, 2004. [2] G. McComb, <i>Robot Builder's Bonanza</i> , 4 <sup>th</sup> Edition, McGraw-Hill, 2011. [3] T. Braünl, <i>Embedded Robotics: Mobile Robot Design and Applications with Embedded Systems</i> , 3 <sup>rd</sup> Edition, Springer, 2008.		



## 3-2-16 Data Communication (CE361)

Data Communication (CE361)		
Units	Prerequisites	Corequisites
3	Computer Networks, Signal and Systems	---
<b>Objectives:</b> This course examines the technologies for the implementation of computer and communication networks. Students will be introduced to the principles of digital transfer, shared media technologies, Ethernet and wireless local area networks, and advanced computer network architectures.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Overview of computer networks</li><li>○ Internet networks</li><li>○ OSI and TCP / IP layer models</li></ul></li><li>• Principles of digital transmission<ul style="list-style-type: none"><li>○ Digital representation of information</li><li>○ Digital channel characteristics</li><li>○ Nyquist rate for signal transmission</li><li>○ Shannon's channel capacity</li><li>○ Digital modulation</li><li>○ Media features and digital transmission systems</li></ul></li><li>• Error detection and correction methods<ul style="list-style-type: none"><li>○ FEC and ARQ error detection methods</li><li>○ Error detection and correction codes</li><li>○ ARQ error control protocols</li></ul></li><li>• Data Link Layer<ul style="list-style-type: none"><li>○ Introduction of Data Link Layer Services</li><li>○ Point-to-Point Protocol</li><li>○ HDLC Protocol</li></ul></li><li>• Medium Access Control<ul style="list-style-type: none"><li>○ Introducing shared channels and point-to-point channels</li><li>○ Random access methods</li><li>○ Scheduling methods</li><li>○ Channelization methods</li></ul></li></ul>		



- Local Area Networks
  - IEEE 802 standards for local area networks
  - Ethernet networks
  - Wireless local area networks
  - Local area network bridge
  - Ethernet switches and VLANs
- Advanced computer network architectures
  - Label switching and MPLS
  - Software-Defined networks

**References:**

- [1] A. Leon-Garcia, I. Widjaja, *Communication Networks*, 2<sup>nd</sup> Edition, McGraw-Hill, 2003.
- [2] W. Stallings, *Data and Computer Communications*, 10<sup>th</sup> Edition, Pearson, 2014.
- [3] J F. Kurose, K. W. Ross, *Computer Networking: A Top-Down Approach*, 7<sup>th</sup> Edition, Pearson, 2016.
- [4] A. S. Tanenbaum, *Computer Networks*, 5<sup>th</sup> Edition, Pearson, 2010.
- [5] B. A. Forouzan, D. College, *Data Communications and Networking*, 5<sup>th</sup> Edition, McGraw-Hill, 2012.



## 3-2-17 Information and Communications Security (CE362)

Information and Communications Security (CE362)		
Units	Prerequisites	Corequisites
3	Computer Networks	---
<b>Objectives:</b> The main objective of the course is security in information technology and network communications. In more details, the students will learn foundations of security and security mechanisms, specially cryptography, authentication, and security challenges and solutions in network layers.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Security definition and dimensions</li><li>○ Threats and attacks categories</li></ul></li><li>• Symmetric Cryptography</li><li>• Message Authentication and Public key cryptography</li><li>• User Authentication</li><li>• Email Security (PGP, S/MIME)</li><li>• Transport Layer Security</li><li>• IP security and VPN</li><li>• Intrusion detection systems and firewalls</li><li>• Malicious codes and malwares</li></ul>		
<b>References:</b> [1] W. Stallings, <i>Network Security Essentials, Application and Standard</i> , 6 <sup>th</sup> Edition, Prentice-Hall, 2017. [2] T. R. Peltier, <i>Information Security Fundamentals</i> , 2 <sup>nd</sup> Ed., CRC Press, 2014. [3] M. Goodrich, R. Tamassia, <i>Introduction to Computer Security</i> , Pearson Education, 2014.		



## 3-2-18 Multimedia Systems (CE363)

Multimedia Systems (CE363)		
Units	Prerequisites	Corequisites
3	Engineering Probability and Statistics, Signal and Systems	---

### Objectives:

Multimedia content is one of the main ingredients of any network-connected computer system. Multimedia systems play an ever-increasing role in most aspects of information technology, computer architecture, computer software, computer networks and application programs. The main goal of this course is to introduce the principles of multimedia systems, applications and communication. The students are to learn the acquisition, representation, compression and transfer of multimedia data. In addition, current standard data compression techniques and standards are covered. We also discuss challenges such as resilience against packet loss and quality of service in transferring multimedia content over wireless and IP networks.

### Topics:

- Principles of color and digital imaging
- Basics of lossless compression
  - Entropy coding
  - Huffman coding
  - Dictionary coding such as LZW, LZ77
  - Run Length coding
- Basics of lossy compression
  - Vector quantization
  - Rate-distortion relationship
- Fundamentals of compression standards
  - DCT, JPEG, DWT, JPEG2000
- Rate Allocation
- Principles of digital video
- Basics of video compression
- Video compression standards
  - H.261, H.263, H.264, MPEG-1, MPEG-2, MPEG-4
- Principles of digital audio
- Standards of audio compression
  - AC-3, AAC, MP3
- MPEG Systems
- Principles and standards of multimedia conferencing



- SIP and H.32x
- Principles of multimedia transfer over computer and telephone networks
  - Error detection and recovery
  - Error concealing
  - Resilience
  - Quality of service (QoS)
  - Quality of experience (QoE)
  - Multicasting, and streaming

**References:**

- [1] M. Ghanbari, *Standard Codecs: Image Compression to Advanced Video Coding*, 3<sup>rd</sup> Edition., Institution of Engineering and Technology, 2011.
- [2] R. Steinmetz, K. Nahrstedt, *Multimedia Systems*. Springer, 2010.
- [3] T. E. Richardson. *The H.264 Advanced Video Compression Standard*, 2<sup>nd</sup> Edition, Willey, 2010.



## 3-2-19 Mobile Device Programming (CE364)

Mobile Device Programming (CE363)		
Units	Prerequisites	Corequisites
3	Advanced Programming	---
<b>Objectives:</b> The purpose of this course is to familiarize students with open source initiative and software for mobile devices. After general introduction to mobile development in different platforms, in particular, students will learn about Android operating system as well as Android programming environment. Wide range of topics including mobile devices hardware architecture, software architecture, operating systems, programming languages and development tools are covered. Through exercises and projects, students would gain skills necessary to develop and run a software application on a mobile device.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction<ul style="list-style-type: none"><li>○ Mobile devices</li><li>○ Mobile applications</li></ul></li><li>• Introduction to Android<ul style="list-style-type: none"><li>○ Android Features</li><li>○ Architecture</li><li>○ Application Components</li></ul></li><li>• Android programming basics<ul style="list-style-type: none"><li>○ Android Installation and development tools</li></ul></li><li>• Android UI, Views, and Widgets</li><li>• Intent, Permissions, and Fragment concepts</li><li>• Storing, retrieving, and managing information</li><li>• Threads and Asynchronous tasks</li><li>• Notifications and Alarms</li><li>• Content Providers</li><li>• Event handling</li><li>• Camera handling</li><li>• State management</li><li>• Service</li><li>• Broadcast Receiver</li><li>• Graphic and animation</li></ul>		





- Multimedia
- Geo-Location handling
- Custom Components

**References:**

- [1] D. Griffiths, D. Griffiths, *Head First Android Development: A Brain-Friendly Guide*, 1<sup>st</sup> Edition, O'Reilly, 2015.
- [2] Z. Mednieks, L. Dornin, *Programming Android: Java Programming for the New Generation of Mobile Devices*, O'Reilly, 2012.
- [3] M. L. Murphy, *The Busy Coder's Guide to Advanced Android Development*, 8<sup>th</sup> Edition, CommonsWare, 2009.
- [4] J. Iversen, M Eierman, *Learning Mobile App Development*, Addison-Wesley, 2011.



## 3-2-20 Information Retrieval (CE421)

Information Retrieval (CE421)		
Units	Prerequisites	Corequisites
3	Data Structures and Algorithms, Engineering Probability and Statistics	---
<b>Objectives:</b> Information retrieval is the process through which a computer system can respond to a user's query for text-based information on a specific topic. Web search is one of the most important applications of information retrieval techniques and an area in which most people interact with IR systems. The goal of this course is to introduce students with the basics, models, tools and applications of the modern information retrieval.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Text Preprocessing and vocabulary construction<ul style="list-style-type: none"><li>○ Document and word separation</li><li>○ Normalization</li><li>○ Stemming and lemmatization</li><li>○ Spelling correction</li></ul></li><li>• Indexing<ul style="list-style-type: none"><li>○ Index construction</li><li>○ Index compression</li></ul></li><li>• Retrieval and ranking methods<ul style="list-style-type: none"><li>○ Boolean, Vector-based, Probabilistic Retrieval</li></ul></li><li>• Performance evaluation for information retrieval methods</li><li>• Query languages and operators</li><li>• Document classification and clustering</li><li>• Web search<ul style="list-style-type: none"><li>○ Basics</li><li>○ Crawling</li><li>○ Link analysis</li></ul></li><li>• IR-based applied systems</li></ul>		
<b>References:</b> [1] C. D. Manning, P. Raghavan, H. Schütze, <i>Introduction to Information Retrieval</i> , Cambridge University Press. 2008.		



## 3-2-21 Fundamentals of Cloud Computing (CE422)

Fundamentals of Cloud Computing (CE422)		
Units	Prerequisites	Corequisites
3	Computer Networks, Operating Systems	---
<b>Objectives:</b> In this course, students will learn the basic concepts of cloud computing, the architecture of cloud computing infrastructure and knowhow of cloud-based applications creation. Virtualization technologies in computing, networking and data storage and fundamental concepts of cloud computing are also covered.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• The basic concepts of cloud computing</li><li>• A brief history and evolution of cloud computing</li><li>• Service models and standards</li><li>• OpenStack<ul style="list-style-type: none"><li>○ Architecture and building blocks</li><li>○ Console and controller, Scheduler, Nova cert., Cinder services, Swift object storage, Neutron networking, Keystone, Glance image services, Heat configuration service</li></ul></li><li>• Hadoop HDFS distributed file system<ul style="list-style-type: none"><li>○ Hadoop MapReduce</li><li>○ Hadoop Zookeeper, Oozie, Flume, Sqoop</li></ul></li><li>• Cloud databases and storage technologies<ul style="list-style-type: none"><li>○ HiveQL, Hive, Cassandra, MongoDB, HBase</li></ul></li><li>• Cloud computing infrastructure<ul style="list-style-type: none"><li>○ Data center architecture</li><li>○ Storage and networking technologies</li></ul></li><li>• Virtualization<ul style="list-style-type: none"><li>○ Virtualization concepts and techniques</li><li>○ Virtual SAN, Distributed virtual switch, Virtual machine, Containers</li></ul></li></ul>		
<b>References:</b> [1] K. Chandrasekaran, <i>Essentials of cloud computing</i> , CRC Press, 2014 [2] N. B. Ruparelia, <i>Cloud Computing</i> , MIT Press, 2016 [3] T. Erl, Z. Mahmood, R. Puttini, <i>Cloud Computing, Concepts, Technology and Architecture</i> , Prentice Hall, 2013. [4] I. Ganelin, E. Orhian, K. Sasaki, B. York, <i>Spark: Big Data Cluster Computing in Production</i> , Wiley, 1 <sup>st</sup> Edition, 2016.		



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- [5] T. White, *Hadoop: The Definitive Guide: Storage and Analysis at Internet Scale*, 4<sup>th</sup> Edition. O'Reilly Media, 2015.
- [6] H. Geng, *Data Center Handbook*. Wiley, 1<sup>st</sup> Edition, 2014.



## 3-2-22 Software Testing (CE431)

Software Testing (CE431)		
Units	Prerequisites	Corequisites
3	Software Engineering II	---
<b>Objectives:</b> The goal of this course is to familiarize students with software testing method, especially those methods that generate test data. Furthermore, in this course, tools required for software testing are introduced.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to testing methods</li><li>• Coverage criteria</li><li>• Graph based criteria</li><li>• Logic-based criteria</li><li>• Inventory-based criteria</li><li>• Syntax-based criteria</li><li>• Practical considerations for software testing</li><li>• Software testing and adjustment tools for testing</li></ul>		
<b>References:</b> [1] P. Ammann, J. Offutt, <i>Introduction to software testing</i> , Cambridge University Press, 2016. [2] P. C. Jorgensen, <i>Software Testing: A Craftsman's Approach</i> , 2 <sup>nd</sup> Edition, CRC Press, 2002.		



## 3-2-23 User Interface Design (CE432)

User Interface Design (CE432)		
Units	Prerequisites	Corequisites
3	Software Engineering I	---
<b>Objectives:</b> <p>This course provides an introduction to design user interfaces. It covers design principles, prototyping techniques, evaluation techniques, implementation of user interfaces, wireframes, color schemes, tones, and design templates. Moreover, it describes how to obtain information from a client to design and manufacture interfaces for different platforms, such as mobile, tablet and desktop. Finally, it briefly investigates well-known tools for generating and upgrading user interfaces.</p>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to user interface design<ul style="list-style-type: none"><li>○ Evolution of user interfaces</li><li>○ Current trends in user interfaces design</li></ul></li><li>• Principles of user interface design<ul style="list-style-type: none"><li>○ Usability, Learnability, Visibility, Efficiency, ...</li></ul></li><li>• User Interface basic concepts<ul style="list-style-type: none"><li>○ Color schemes and tones</li><li>○ Typography</li><li>○ Layouts</li><li>○ Wireframes and mockups</li><li>○ Navigation</li></ul></li><li>• User interface design process<ul style="list-style-type: none"><li>○ User interface design methodology</li><li>○ Iterative design</li><li>○ User-centered design</li></ul></li><li>• User interface design techniques<ul style="list-style-type: none"><li>○ Sketching, scenarios, storyboards</li><li>○ Design patterns</li><li>○ Prototyping</li></ul></li><li>• User interface validation and verification</li><li>• User interface evaluation techniques</li><li>• User interface tools</li></ul>		



## References:

- [1] H. R. Hartson, P. S. Pyla, *The UX Book: Process and Guidelines for Ensuring a Quality User Experience*, Morgan Kaufmann / Elsevier, 2012.
- [2] B. Shneiderman, C. Plaisant. *Designing the User Interface: Strategies for Effective Human-Computer Interaction*, 4<sup>th</sup> Edition, Addison Wesley, 2004.
- [3] J. Preece, Y. Rogers, H. Sharp, *Interaction Design: Beyond Human-computer Interaction*, 4<sup>th</sup> Edition, John Wiley & Sons Ltd, 2002.
- [4] D. A. Norman, *The Design of Everyday Things*. Basic Books, 2002.
- [5] A. J. Dix, J. E. Finlay, G. D. Abowd, R. Beale, *Human-Computer Interaction*. 2<sup>nd</sup> Edition. Prentice Hall, 1998.
- [6] D. R. Olsen, *Developing User Interfaces (Interactive Technologies)*. Morgan Kaufmann, 1998.



## 3-2-24 Hardware-Software Codesign (CE441)

Hardware-Software Codesign (CE441)		
Units	Prerequisites	Corequisites
3	Computer Architecture	---
<b>Objectives:</b> This course provides a basic insight on designing computing systems by joint consideration of software and hardware subsystems based on state-of-the-art system design methodologies. In addition, students learn designing models of hardware and software components in different abstraction layers as well as communication between application program and hardware.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction</li><li>• System-level design (approaches and applications)</li><li>• An introduction to a system-level language (SystemC)</li><li>• Dataflow and control models</li><li>• Transaction-level models</li><li>• System-level synthesis and verification</li><li>• Hardware-software interface design</li><li>• Codesign optimization approaches</li><li>• Codesign implementation platform</li><li>• Behavioral synthesis</li></ul>		
<b>References:</b> [1] P. Schaumont, <i>A Practical Introduction to Hardware/Software Codesign</i> , Springer, 2013. [2] F. Vahid, T. Givargis, <i>Embedded System Design: A Unified Hardware/Software Introduction</i> , John Wiley & Sons, 2002. [3] G. De Micheli, R. Ernst, W. Wolf, <i>Readings in Hardware/Software Codesign</i> , Morgan Kaufman, 2002.		





## 3-2-25 Introduction to Bioinformatics (CE451)

Introduction to Bioinformatics (CE451)		
Units	Prerequisites	Corequisites
3	Foundations of Computational Intelligence	---
<b>Objectives:</b> The main goal of this course is to introduce basic concepts and problems in bioinformatics and related algorithms and solutions. Because of wide and diversity of concepts in bioinformatics, this course intends to make students familiar with various concepts and sub domains in the field, so students become ready to enter in more specialized fields and carry out state-of-the-art research. On the other hand, this course, via practical assignments and projects, helps students develop basic skills required for practical careers, such as working with common bioinformatics databases and software.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to bioinformatics and its major domains</li><li>• Review of some basic concepts in biology</li><li>• Access to sequence data and related information</li><li>• Pairwise sequence alignment</li><li>• BLAST</li><li>• Advanced database searching</li><li>• Multiple sequence alignment</li><li>• Molecular phylogeny and evolution</li><li>• DNA: The eukaryotic chromosome</li><li>• Analysis of next-generation sequence data</li><li>• Bioinformatic approaches to RNA</li><li>• Gene expression: Microarray and RNA-seq data analysis</li><li>• Protein analysis and proteomics</li><li>• Protein structure</li><li>• Functional genomics</li><li>• Introduction to systems biology</li></ul>		
<b>References:</b> [1] J. Pevsner, <i>Bioinformatics and Functional Genomics</i> , John Wiley & Sons, 2015. [2] A. Lesk, <i>Introduction to Bioinformatics</i> , Oxford University Press, 2013. [3] M. Zvelebil, J. Baum. <i>Understanding Bioinformatics</i> , Garland Science, 2007.		



## 3-2-26 Data Mining (CE452)

Data Mining (CE452)		
Units	Prerequisites	Corequisites
3	Foundations of Computational Intelligence	---
<b>Objectives:</b> Data mining provides methods for exploring data types and discovering knowledge from data. After general overview of data mining problems, this course deals with data preparation methods, mining association rules, clustering, classification and outlier detection. The main objectives of this course are as follow: <ul style="list-style-type: none"><li>• Understanding algorithms and data mining techniques</li><li>• Developing data mining applications with existing data mining tools and programming languages</li><li>• Learn to analyze and visualize data mining results</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction (motivations and challenges)</li><li>• Data warehouse</li><li>• Types and characteristics of data sets</li><li>• Data preprocessing and preparation<ul style="list-style-type: none"><li>○ Data cleaning</li><li>○ Outlier detection</li><li>○ Normalization,</li><li>○ Discretization</li><li>○ Dimensionality reduction</li><li>○ Regression</li></ul></li><li>• Association rule mining<ul style="list-style-type: none"><li>○ Apriori</li><li>○ FP-growth algorithms</li></ul></li><li>• Clustering<ul style="list-style-type: none"><li>○ Partitioning-based</li><li>○ Hierarchical</li><li>○ Density-based algorithms</li></ul></li><li>• Classification<ul style="list-style-type: none"><li>○ Decision tree and naïve Bayes</li><li>○ Evaluation methods</li></ul></li><li>• Data summarization and visualization</li></ul>		



- Outlier detection methods
- Recommender systems
  - Collaborative filtering
  - Content-based
- Advanced topics in data mining

**References:**

- [1] J. Han, M. Kamber, J. Pei, *Data Mining: Concepts and Techniques*, 3<sup>rd</sup> Edition, McGraw-Hill 2012.
- [2] T. Mitchell, *Machine Learning*, McGraw-Hill, 1997



## 3-2-27 Internet of Things Fundamentals (CE461)

Internet of Things Fundamentals (CE461)		
Units	Prerequisites	Corequisites
3	Microprocessor and Assembly Language, Computer Networks	---
<b>Objectives:</b> This course introduces Internet of Things (IoT) concepts, applications, architecture and its corresponding technologies as well as network protocols. Students will also learn how to choose an appropriate network for IoT applications, and acquire skill for designing, implementing, and debugging an IoT-based system. This course provides students with foundational skills by doing mini-projects.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to IoT<ul style="list-style-type: none"><li>○ IoT Applications</li><li>○ IoT ecosystem</li><li>○ IoT architecture and standards</li></ul></li><li>• IoT Hardware<ul style="list-style-type: none"><li>○ Embedded systems and their application to IoT</li><li>○ Arduino platform and C programming</li><li>○ Introduction to the Raspberry platform, Raspbian operating system, and python programming, Connecting Raspberry board to Internet</li><li>○ IoT lab exercises</li></ul></li><li>• IoT Connectivity and networks<ul style="list-style-type: none"><li>○ Physical and Mac layers technologies</li><li>○ Network, transport, and application layers protocols</li><li>○ IoT lab exercises</li></ul></li><li>• IoT platforms<ul style="list-style-type: none"><li>○ Introduction of existing open-source and commercial platforms</li><li>○ Connecting devices to platform</li><li>○ Creating an application for IoT</li><li>○ Data analytics</li><li>○ IoT lab exercises</li></ul></li><li>• Security and privacy in IoT</li></ul>		
<b>References:</b> [1] O. Hersent, D. Boswarthick, O. Elloumi, <i>The Internet of Things: Key Applications and Protocols</i> , 2 <sup>nd</sup> Edition, Wiley, 2013.		



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[2] M. Margolis, *Arduino Cookbook*, 3<sup>rd</sup> Edition, O'Reilly, 2017.

[3] S. Monk, *Raspberry Pi Cookbook*, 2<sup>nd</sup> Edition, O'Reilly, 2016.



## 3-3 Optional Courses

### 3-3-1 Computer Graphics (CE371)

Computer Graphics (CE371)		
Units	Prerequisites	Corequisites
3	Advanced Programming	---
<b>Objectives:</b> The objective of this course is to introduce the basic principles of computer graphics with the emphasize on game development. In this course, the techniques of the real-time animation are covered. Students learn the main techniques including pipeline, lighting, shader, post processing, HDR, Bloom, and etc.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Mathematics of computer graphics</li><li>• Introduction to graphic pipeline</li><li>• Sprite tiling</li><li>• Basic geometry</li><li>• Ray tracing</li><li>• Shader</li><li>• Rasterization</li><li>• Global Illumination</li><li>• HDR †DOF †Tome Mapping</li><li>• Deferred rendering</li></ul>		
<b>References:</b> [1] T. Akenine-Moller, E. Haines, N. Hoffman, <i>Real-Time Rendering</i> , 3 <sup>rd</sup> Edition, O'Reilly, 2008 [2] A. Sherrod, <i>Game Graphics Programming</i> , O'Reilly, 2008		



## 3-3-2 Human and Computer Interaction (CE372)

Human and Computer Interaction (CE372)		
Units	Prerequisites	Corequisites
3	Software Engineering I	---
<b>Objectives:</b> The goal of this course is to familiarize students with foundations of human-computer interaction (HCI), complexity of humans and their attitude to digital instruments, computer and digital tools and accessories, different stages of simple and reliable software design based on intrinsic perception of humans, and management issues at different stages of software design. The course also discusses future of HCI and its impact on software development, and HCI concepts and issues.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Human-Computer Interaction (HCI) concepts</li><li>• Modeling at HCI</li><li>• Framework concepts</li><li>• Designing interactive frameworks</li><li>• Design language</li><li>• Design evaluation (assessment)</li><li>• Designing details</li><li>• Methods to improve design details</li></ul>		
<b>References:</b> [1] B. Shneiderman, C. Plaisant, <i>Designing the User Interface: Strategies for Effective Human-Computer Interaction</i> , 4 <sup>th</sup> Edition, Pearson Education, 2005.		



## 3-3-3 Robot Building Workshop (CE373)

Robot Building Workshop (CE373)		
Units	Prerequisites	Corequisites
3	Introduction to Robotics	---
<b>Objectives:</b> This workshop is designed to familiarize the students with different steps of building a robot, including the mechanical and electrical parts. In addition to building the robot components, the students learn the assembling and testing of the built system. The testing phase includes programming and evaluating various components of the system, including the operation of different robot sensors and drives.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Familiarization with technical documents of electrical and mechanical components</li><li>• Capabilities and constraints of metal shaping machines</li><li>• Product assembly and test steps</li><li>• Selecting electronic components and building printed circuit boards</li><li>• Electric AC and DC motor drives and their control via microcontrollers</li><li>• Capabilities of microcontrollers and their programming</li><li>• Step by step testing and fault detection of a mechatronic system</li></ul>		
<b>References:</b> [1] <i>C Programming Reference Manual</i> [2] <i>Introduction to Micro AVR</i> [3] <i>Lab manual</i>		





## 3-3-4 Computer Games Design (CE374)

Computer Games Design (CE374)		
Units	Prerequisites	Corequisites
3	Advanced Programming	---
<b>Objectives:</b> The objective of this course is to introduce the principles of computer game design and development. This course covers these topics: Process and pipeline of game development, game engines, fundamental aspect of computer games, mathematical and physical sciences of computer games.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Analysis of the structure of computer games</li><li>• Introduction to the game development pipeline</li><li>• Architecture of game engines</li><li>• Mathematical foundations of computer games</li><li>• Introduction to Engine Support System</li><li>• Introduction to Game Loop</li><li>• Programming and development of game</li><li>• Artificial intelligence role in games</li><li>• Game development middleware</li></ul>		
<b>References:</b> [1] J. Gregory, <i>Game Engine Architecture</i> , A K Peters LTD, 2009. [2] E. Lengyel, <i>Game Engine series</i> , CRC Press, 2007-2010 [3] D. H. Eberly, <i>3D Game Engine Design</i> , 2 <sup>nd</sup> Edition, Morgan Kaufman, 2006		



## 3-3-5 Theory of Computation (CE375)

Theory of Computation (CE375)		
Units	Prerequisites	Corequisites
3	Theory of Languages and Machines	---
<b>Objectives:</b> The goal of this course is to familiarize students with the foundations of theory of computation and fundamental concepts of computability models, unsolvable problems, mathematical logics and theory of automata on finite string or tree as input. This course provides necessary theoretical foundations for graduate students who are interested in theory of computation and algorithms, formal methods in software engineering, verification of systems and artificial intelligence.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Computability Theory and Computation Complexity<ul style="list-style-type: none"><li>○ Turing computable Model, Church-Turing Thesis, Functions and Decidable Languages, Functions and Recognizable Languages, Uncomputable Functions, Halting Problem, Universal Turing Machine, Multi-tape Turing Machine, Non-deterministic Machine and Theorems for Their Equivalency</li><li>○ Proof Methods for Undecidability and Unrecognizability of Languages (Method for Reduction to Halting Problem and Functional Reduction Method)</li><li>○ Introduction to other Computational Models (von Neumann Random Access Model, Kleene's Recursive Theory, Lambda-Church Calculus, Post's Correspondence Problem)</li><li>○ Recursive Theorem and Self-Reference</li><li>○ Information-Theoretic Computational Complexity and String Complexity</li><li>○ Introduction to Complexity Theory, Review of Classes of Time and Storage Complexity Classes and Hard Problems</li></ul></li><li>• Mathematical Logic from Computation Theory Point of View<ul style="list-style-type: none"><li>○ Propositional Logic and its Syntax and Semantic, Axiomatic Inference System and its Soundness and Completeness Theorems, Undecidable Theorems for Propositional Logic</li><li>○ First Order Logic and its Syntax and Semantic, Compression and Löwenheim-Skolem Theorems</li><li>○ Axiomatic Inference System of First Order Logic and its Soundness Theorem</li><li>○ Godel's Completeness Theorem for First Order Logic Inference System</li><li>○ Church's Theorem in Undecidability of First Order Logic</li><li>○ Axiomatic System for Theory of Numbers and Godel's Incompleteness Theorem</li></ul></li><li>• An Introduction on Theory of Automata on Infinite inputs<ul style="list-style-type: none"><li>○ Buchi and Rabin Automata on Finite Strings</li></ul></li></ul>		



- Theorems for Complementation and Empty Test for Language of Buchi Automata, Deterministic Buchi Automata, Safra's Theorem
- An Introduction to Relationship of Decidable Problems in Logic with Theory of Automata
- An Introduction to Automata on Tree as input

**References:**

- [1] M. Divis, R. Sigal, E. Weyuker, *Computability, Complexity, and Languages*. 2<sup>nd</sup> Edition, Academic Press, 1997.
- [2] M. Sipser, *Introduction to the Theory of Computation*. 2<sup>nd</sup> Edition, Thompson, 2006.



## 3-3-6 Computer Simulation (CE376)

Computer Simulation (CE376)		
Units	Prerequisites	Corequisites
3	Operating Systems, Engineering Probability and Statistics	---
<b>Objectives:</b> This course covers the concepts and principles of computer simulation. A system behavior is implemented in a required level of abstraction in a virtual environment in simulation. This provides a flexible and low cost approach to study the behavior of systems. Application of computer simulation covers a wide range from educational purposes to evaluating system design and tuning. In this course, students are acquainted with simulator design and implementation using programming languages and also learn to use an existing simulation package. The statistical techniques used in simulations will also be covered including input-output modeling and simulation validation. Variance reduction techniques and rare event simulation are introduced as well.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Basic concepts and ten main steps in a simulation study</li><li>• Basics of discrete event simulation (DES)</li><li>• Examples of software simulation</li><li>• A review of simulation packages</li><li>• Introducing a simulation package</li><li>• Statistical models in simulation</li><li>• Validating a simulation model</li><li>• Analysis of simulation outputs</li><li>• Evaluation and optimization of alternative system designs</li><li>• Case studies: computer systems and networks</li><li>• Variance reduction techniques and rare event simulation</li></ul>		
<b>References:</b> [1] J. Banks, J. S. Carson, B. L. Nelson, D. M. Nicol, <i>Discrete-Event System Simulation</i> , 5 <sup>th</sup> Edition, Pearson Publishing, 2010.		



## 3-3-7 Foundations of Computer Animation (CE377)

Foundations of Computer Animation (CE377)		
Units	Prerequisites	Corequisites
3	Computer Graphics	---
<b>Objectives:</b> The objective of this course is introducing the basic rules and foundations of animation which is the indispensable part of computer games. Introduction to principles of generating, processing and displaying animations are the main section of the course. Along the theoretical aspects, students learn to combining, processing and displaying animations in practice.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• History of computer animation</li><li>• Hierarchical rigid body animation</li><li>• Skeleton meshes</li><li>• Vertex based animation</li><li>• Kinematics</li><li>• Blending</li><li>• Layering</li><li>• Bone placement</li><li>• Mixing</li><li>• Natural motions</li><li>• Pipeline</li></ul>		
<b>References:</b> [1] C. Grenberg, <i>Character Animation with DirectX</i> , Charles River Media, 2009. [2] J. Gregory, <i>Game Engine Architecture</i> , A K Peters LTD, 2009.		



## 3-3-8 Information Technology Project Management (CE378)

Information Technology Project Management (CE378)		
Units	Prerequisites	Corequisites
3	---	---
<b>Objectives:</b> The objective of this course is to provide a foundation to prepare students, as future IT project managers, IT engineers, or system architects, to play leading roles in the application and management of e-business system construction. Upon successful completion of the course, students will be able to: <ul style="list-style-type: none"><li>• Understand the job roles of an IT project manager;</li><li>• Develop Work Breakdown Structures (WBS)</li><li>• Manage overall change control</li><li>• Control project execution processes</li><li>• Build up the baseline knowledge for further career in IT project management fields</li></ul>		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to Project Management</li><li>• The Project Management and Information Technology Context</li><li>• The Project Management Process Groups: A Case Study</li><li>• Project Integration Management</li><li>• Project Scope Management</li><li>• Project Time Management</li><li>• Project Cost Management</li><li>• Project Quality Management</li><li>• Project Human Resource Management</li><li>• Project Communications Management</li><li>• Project Risk Management</li><li>• Project Procurement Management</li><li>• Project Stakeholder Management</li><li>• Guide to Using Microsoft Project</li></ul>		
<b>References:</b> [1] K. Schwalbe, <i>Information Technology Project Management</i> . 7 <sup>th</sup> Edition, Course Technology, 2014. [2] J. T. Marchewka, <i>Information Technology Project Management</i> , 4 <sup>th</sup> Edition, Wiley, 2012.		



## 3-3-9 E-Commerce (CE379)

E-Commerce (CE379)		
Units	Prerequisites	Corequisites
3	Engineering Economics, Computer Networks	---
<b>Objectives:</b> In this course, students are introduced to the basic concepts of e-commerce. They will learn about the application of information technology in e-commerce. Moreover, in this course, the concepts and techniques of internet marketing is discussed.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction to E-commerce</li><li>• Knowledge based business</li><li>• Value in network economy</li><li>• Virtual enterprise and factory</li><li>• Product development in digital economy</li><li>• Marketing in digital economy</li><li>• Product management and trading services</li><li>• Strategic planning and trading process</li><li>• Security and E-commerce</li><li>• E-commerce infrastructure</li><li>• E-commerce software</li><li>• Search strategies</li><li>• Application of software agents in commerce</li></ul>		
<b>References:</b> [1] G. W. Treese, and L.C. Stewart, <i>Designing System for Internet Commerce</i> . 1 <sup>st</sup> Edition, Addison Wesley, 1998. [2] D. Coyle, <i>The Weightless World: Strategies for Managing the Digital Economy</i> . The MIT Press, 1998. [3] A. Chaudhury, J.P. Kuilboer, <i>E-Business &amp; E-Commerce Infrastructure: Technologies Supporting the E-Business Initiative</i> . McGraw-Hill, 2011.		



## 3-3-10 Startup Business Development (CE380)

Startup Business Development (CE380)		
Units	Prerequisites	Corequisites
3	Software Engineering I	---
<b>Objectives:</b> The purpose of this course is introducing new methods, tools, and concepts in business development especially startups. We tried to outline starting a personal business for students by presenting practical concepts and internal and external examples and increase success rate of such businesses by teaching some of the methods and sharing experiences in main centers of technology growth. In addition to subjects, entrepreneurs or successful managers will be invited in some sessions not only to teach some of the subjects or related topics but to answer students' questions. During this course, students start developing a business plan for a startup practically and in groups.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction</li><li>• Startup businesses</li><li>• Risk evaluation</li><li>• Resource evaluation</li><li>• Market analysis</li><li>• Financial analysis</li><li>• Proposed value</li><li>• Team work skills</li><li>• Internal and international business regulations</li><li>• Software development methods and skills</li><li>• Presentation of successful entrepreneurs in Iran and over the world</li><li>• Presentation of great successes and failures in launching startups</li><li>• Lectures from successful startup launchers</li></ul>		
<b>References:</b> [1] A. Osterwalder, Y. Pigneur, <i>Business Model Generation</i> , John Wiley, 2010. [2] E. Ries, <i>The Lean Startup: How Today's Entrepreneurs Use Continuous Innovation to Create Radically Successful Businesses</i> , Crown Business, 2011. [3] S. Blank, S. G. Blank, B. Dorf, <i>The Startup Owner's Manual: The Step-by-step Guide for Building a Great Company</i> , K&S Ranch, 2012. [4] A. Maurya, <i>Running Lean: Iterate from Plan A to a Plan That Works</i> , O'Reilly, 2012.		





## 3-3-11 Industrial Automation (CE381)

Industrial Automation (CE381)		
Units	Prerequisites	Corequisites
3	Microprocessor and Assembly Language	---
<b>Objectives:</b> The objectives of this course is to provide students with the concepts of process control, different kinds of industrial controllers, their components and capabilities, sensors, actuators, high definition media interfaces, communications and networks used for transfer of industrial data.		
<b>Topics:</b> <ul style="list-style-type: none"><li>• Introduction and basic concepts</li><li>• Process control and its variants</li><li>• PID controllers</li><li>• Programmable Logic Controllers (PLC) for automation and process control</li><li>• Direct Digital Control (DDC)</li><li>• Supervised Control and Data Acquisition (SCADA)</li><li>• Distributed Control Systems (DCS)</li><li>• Human and Machine Interface in Industrial Automation systems (HMI)</li><li>• Programming methods for Industrial Automation Planning Design Method</li><li>• Sensors: measuring terms, pressure, temperature, flow, proximity sensors, capacitive, inductive, resistive, magnetic and optical sensors</li><li>• Relays and actuators</li><li>• Data communication, protocols, and industrial networks</li></ul>		
<b>References:</b> [1] J. Stenerson, <i>Industrial Automation and Process Control</i> , Prentice Hall, 2002. [2] T. L. M. Bartelt, <i>Industrial Automated System: Instrumentation and Motion Control</i> , Delmar, Cengage Learning, 2010. [3] M. Madhuchhanda, G. S. Sen, <i>Programmable Logic Controllers and Industrial Automation</i> , 2005. [4] R. Shell, <i>Handbook of Industrial Automation</i> , Taylor & Francis, 2000. [5] J. A. Rehg, G. J. Sartori, <i>Programmable Logic Controllers</i> , Prentice Hall Higher Education, 2009. [6] G. Michel, <i>Programmable logic controllers: architecture and application</i> , Wiley, 1990. [7] M. P. Lukas, <i>Distributed control systems: their evaluation and design</i> , Van Nostrand Reinhold Co., 1986. [8] C. Gerber, <i>Implementation and Verification of Distributed Control System</i> , 2011		



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- [9] S. A. Boyer, *Scada: Supervisory Control and Data Acquisition*, International Society of Automation, 2010.
- [10] M. S. Nardone, *Direct Digital Control Systems: Application Commissioning*, Springer, 1999.